# SMT Solvers and Applications

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### **Topics covered in Lecture on SAT Solvers**

### Motivation for SAT/SMT solvers in software engineering

- Software engineering (SE) problems reduced to logic problems
- Automation, engineering, usability of SE tools through solvers

### **☑** High-level description of the SAT/SMT problem & logics

- Rich logics close to program semantics
- Demonstrably easy to solve in many practical cases

### Modern SAT solver architecture & techniques

- DPLL search, shortcomings
- Modern CDCL SAT solver: propagate (BCP), decide (VSIDS), conflict analysis, clause learn, backJump,
- Termination, correctness
- Big lesson: learning from mistakes

### **Topics covered in Lecture on SMT Solvers**

### • Modern SMT solver architecture & techniques

- Rich logics closer to program semantics
- DPLL(T), Combinations of solvers, Over/under approximations

### • My own contributions: STP & HAMPI

- Abstraction-refinement for solving
- Bounded logics
- SAT/SMT-based applications
- Future of SAT/SMT solvers

### Modern SMT Solvers Are SAT Solvers Enough?

### What is SMT

• Satisfiability Modulo Theories. Just a fancy name for a mathematical theory

### Motivations: why we need SMT?

- A satisfiability solver for rich logics/natural theories (Many natural complexity classes have corresponding SATisfiability problems)
- Easier to encode program semantics in these theories
- Easier to exploit rich logic structure, greater opportunity for optimizations

### **SMT Logics**

- Bit-vectors, arrays, functions, linear integer/real arithmetic, strings, non-linear arithmetic
- Datatypes, quantifiers, non-linear arithmetic, floating point
- Extensible, programmable

SAT & SMT is an explosive combo: incredible impact

### What is Logic? Formal Languages and Valid Reasoning

### What is Logic

- A formal language for constructing mathematical formulas with an associated proof system
- Modern logic starts with the works of Boole, De Morgan, Frege, Cantor, Russell.

### What is a formal language in the context of logic

- Well-defined rules for constructing formulas
- Formulas are defined inductively
- Universe of constant and variables
- Terms are constructed out of constants, variables and functions
- Atomic formulas are predicates applied to terms
- Formulas are Boolean combination of atomic formulas
- Appropriate quantification over variables

### First-order Logic A Language for Mathematics (most of it)

### First-order logic

- Functions and predicates are uninterpreted
- FOL has equality
- Quantification only over variables (higher-order logics quantify over functions, predicates)

### Soundness and completeness of first-order logic (Godel, 1930)

- Equivalence between provability and validity
- Axioms  $\vdash A \iff Axioms \models A$

### Undecidability of first-order logic (Turing, Church 1936)

- Hilbert's 23rd problem
- No fully automatic proof system for first-order logic

### Mathematical Theories Peano Arithmetic and Incompleteness

### **Mathematical Theories**

- Functions and predicates are interpreted (and appropriate axioms are added to FOL)
- Peano arithmetic (PA): +,-,\*,/ are the functions. = and < the predicates.
- It is believed to be powerful enough to axiomatize number theory

### Incompleteness theorem (Godel 1931)

- There are true statements that are not provable in a system as powerful as Peano arithmetic, assuming consistency of PA
- For the first time, this result distinguished truth from proof
- Huge impact on logic and computability
- Arithematization of syntax, Encode proofs as numbers, Diagonalize
- G: G is not provable in this Theory T

### Mathematical Theories The SMT Problem

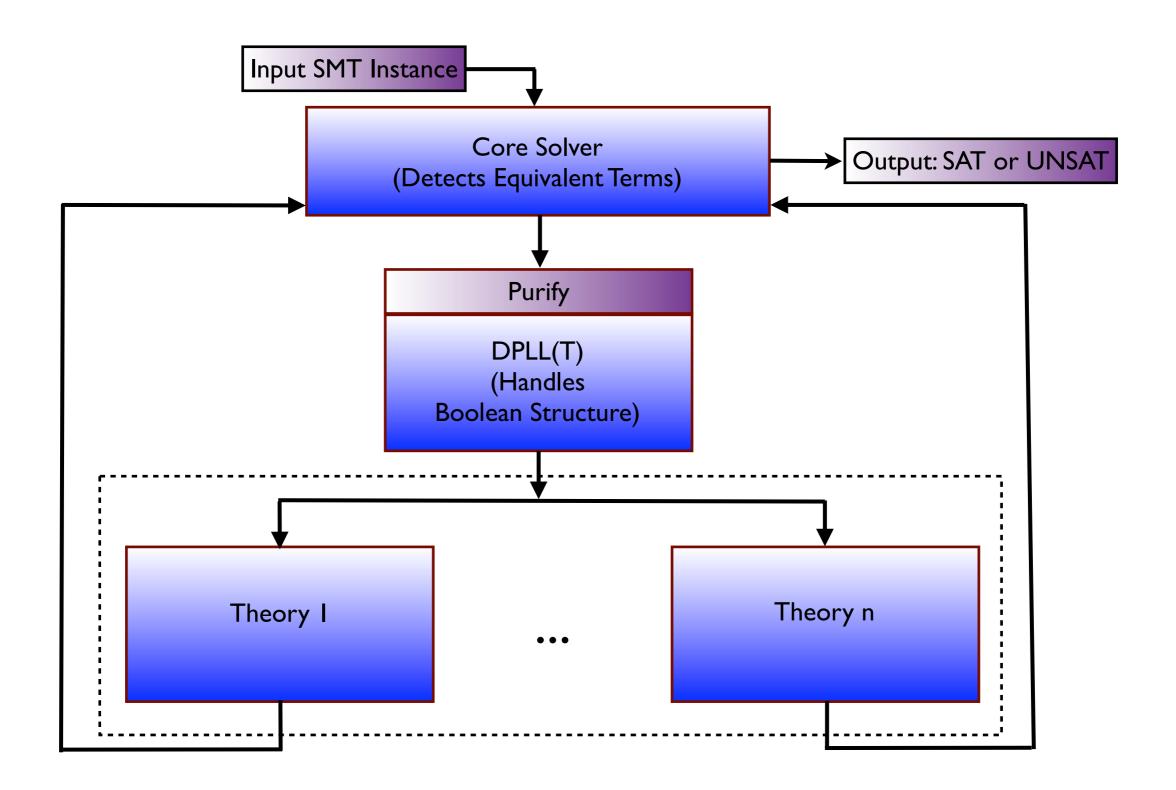
### **Mathematical Theories**

- Peano arithmetic, Presburger, theories of strings, modular arithmetic, quantified Boolean logic
- Different complexity classes: From undecidable, doubly-exponential, all the way down to NP-complete

### SMT problem refers to the satisfiability problems for such rich theories

- Satisfiability modulo-theories
- How do we solve the SAT problem for rich theories efficiently and practically
- Given the difficulty of solving these problems in general, what kind of heuristics are efficient
- Can we play with soundness and completeness in a controlled fashion?
- How do we combine such SATisfiability solvers into a solver for the combined theory
- Quantifiers
- How do we connect these solvers to practical software engineering applications

### Standard-issue SMT Solver Architecture Combination of theories & DPLL(T)



### **Problem Statement**

Combine theory solvers to obtain a solver for a union theory

### **Motivation**

- Software engineering constraints over many natural theories
- Natural theories well understood
- Modularity

### <u>How</u>

- Setup communication between individual theory solvers
- Communication over shared signature
- Soundness, completeness and termination

Example Constraint over Linear Reals (R) and Uninterpreted Functions (UF)

$$f(f(x)-f(y)) = a$$
$$f(0) = a+2$$
$$x = y$$

IDEA: 
$$\Phi_{comb} \Leftrightarrow (\Phi_{T1} \wedge EQ) \wedge (\Phi_{T2} \wedge EQ)$$

- First Step: purify each literal so that it belongs to a single theory
- Second Step: check satisfiability and exchange entailed equalities over shared vars (EQ)
- The solvers have to agree on equalities/disequalities between shared vars

UF
 R

 
$$f(e_1) = a$$
 $e_2 - e_3 = e_1$ 
 $f(x) = e_2$ 
 $e_4 = 0$ 
 $f(y) = e_3$ 
 $e_5 = a + 2$ 
 $f(e_4) = e_5$ 
 $x = y$ 

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 $f(e_4) = e_5$ 
 $e_2 = e_3$ 
 $x = y$ 
 $e_1 = e_4$ 

Example Constraint over Linear Reals (R) and Uninterpreted Functions (UF)

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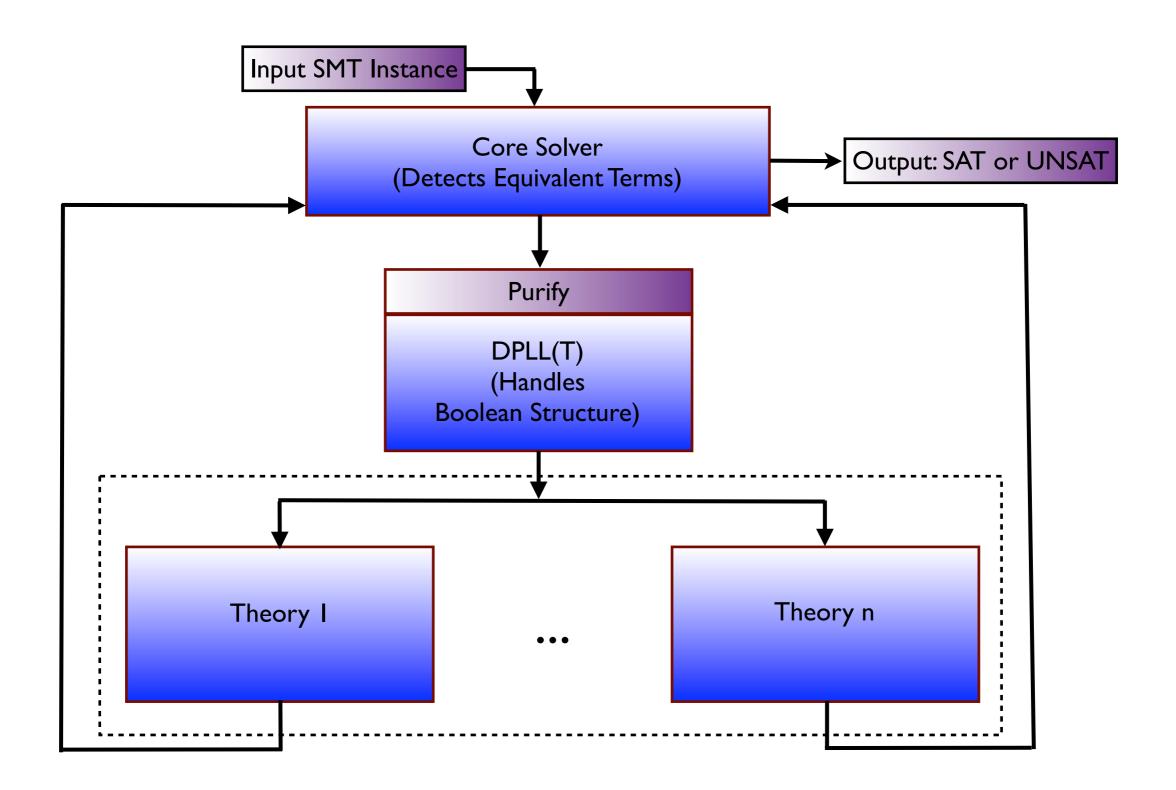
- First Step: purify each literal so that it belongs to a single theory
- Second Step: check satisfiability and exchange entailed equalities over shared vars (EQ)
- The solvers have to agree on equalities/disequalities between shared vars
- UF says SAT, R says UNSAT. Combination returns UNSAT.

UFR
$$f(e_1) = a$$
 $e_2 - e_3 = e_1$  $f(x) = e_2$  $e_4 = 0$  $f(y) = e_3$  $e_5 = a + 2$  $f(e_4) = e_5$  $e_2 = e_3$  $x = y$  $e_5 = a$ 

IDEA:  $\Phi_{comb} \Leftrightarrow (\Phi_{T1} \wedge EQ) \wedge (\Phi_{T2} \wedge EQ)$ 

- Does NOT always work, i.e., does not always give a complete solver
- Example: Cannot combine T<sub>1</sub> with only finite models, and T<sub>2</sub> with infinite models
- Impose conditions on T<sub>1</sub> and T<sub>2</sub>
  - Stably Infinite: If a T-formula has a model it has an infinite model
  - Examples: Functions, Arithmetic
  - Extensions proved to be artificial or difficult
  - Deep model-theoretic implications (Ghilardi 2006, G. 2007)

### Standard-issue SMT Solver Architecture Combination of theories & DPLL(T)



### Standard-issue SMT Solver Architecture DPLL(T)

### **Problem Statement**

• Efficiently handle the Boolean structure of the input formula

### Basic Idea

- Use a SAT solver for the Boolean structure & check assignment consistency against a T-solver
- T-solver only supports conjunction of T-literals

### **Improvements**

- Check partial assignments against T-solver
- Do theory propagation (similar to SAT solvers)
- Conflict analysis guided by T-solver & generate conflict clauses (similar to SAT solvers)
- BackJump (similar to SAT solvers)

### Standard-issue SMT Solver Architecture DPLL(T)

### **Uninterpreted Functions formula**

```
(I) (g(a) = c) \land

(\neg 2 \lor 3) (f(g(a)) \neq f(c) \lor (g(a) = d)) \land

(\neg 4) (c \neq d)
```

### Theory and Unit Propagation Steps by DPLL(T)

```
(Unit Propagate) (I)
(Unit Propagate) (¬4)
(Theory Propagate) (2)
(Theory Propagate) (3)
UNSAT
```

### **History of SMT Solvers**

<u>Category</u>	Research Project	Researcher/Institution/Time Period
Theorem Proving (very early roots of decision procedures)	NuPRL Boyer-Moore Theorem Prover ACL2 PVS Proof Checker	Robert Constable / Cornell / 1970's-present Boyer & Moore / UT Austin / 1970's-present Moore, Kauffmann et al. / UT Austin / 1980's - present Natarajan Shankar / SRI International / 1990's-present
SAT Solvers	DPLL GRASP (Clause learning and backjumping) Chaff & zChaff MiniSAT	Davis, Putnam, Logemann & Loveland / 1962 Marques-Silva & Sakallah / U. Michigan / 1996-2000 Zhang, Malik et al. / Princeton / 1997-2002 Een & Sorensson / 2005 - present
Combinations	Simplify Shostak ICS SVC, CVC, CVC-Lite, CVC3 Non-disjoint theories	Nelson & Oppen / DEC and Compaq / late 1980s Shostak / SRI International / late 1980's Ruess & Shankar / SRI International / late 1990's Barrett & Dill / Stanford U. / late 1990's Tinelli, Ghilardi,, / 2000 - 2008
DPLL(T)	Barcelogic and Tinelli group	Oliveras, Nieuwenhuis & Tinelli / UPC and Iowa / 2006
Under/Over Approximations	UCLID STP	Seshia & Bryant / CMU / 2004 - present Ganesh & Dill / Stanford / 2005 - present
Widely-used SMT Solvers	Z3 CVC4 OpenSMT Yices MathSAT STP UCLID	DeMoura & Bjorner / Microsoft / 2006 - present Barrett & Tinelli / NYU and Iowa / early 2000's - present Bruttomesso / USI Lugano / 2008 - present Deuterre / SRI International / 2005 - present Cimatti et al. / Trento / 2005 - present Ganesh / Stanford & MIT / 2005 - present Seshia / CMU & Berkeley / 2004 - present



### **Topics covered in Lecture 1**

### Motivation for SAT/SMT solvers in software engineering

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- Automation, engineering, usability of SE tools through solvers

### **☑** High-level description of the SAT/SMT problem & logics

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### Modern SAT solver architecture & techniques

- DPLL search, shortcomings
- Modern CDCL SAT solver: propagate (BCP), decide (VSIDS), conflict analysis, clause learn, backJump,
- Termination, correctness
- Big lesson: learning from mistakes

### **Topics covered in Lecture 2**

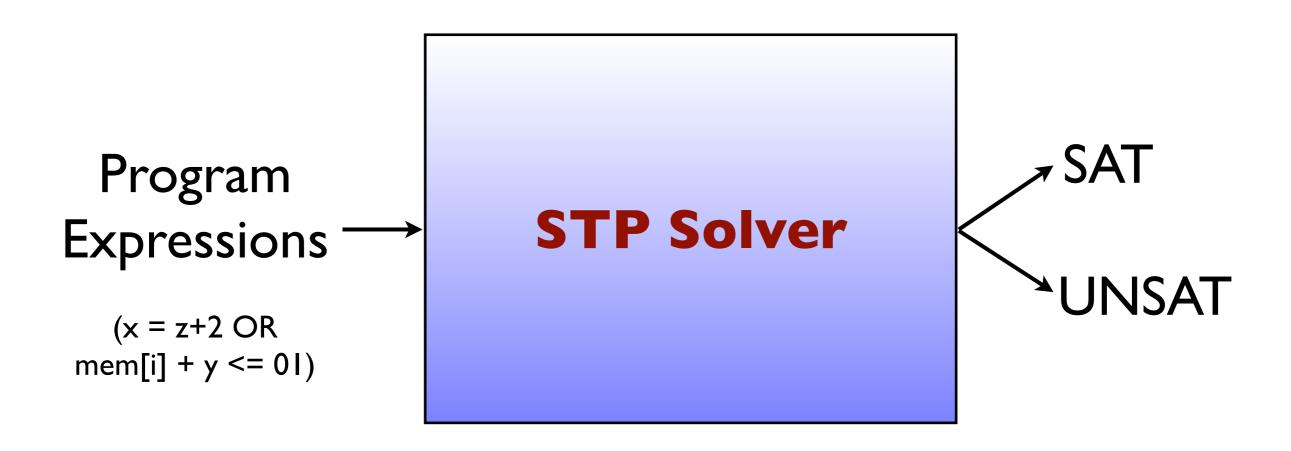
### Modern SMT solver architecture & techniques

- Rich logics closer to program semantics
- DPLL(T), Combinations of solvers, Over/under approximations

### • My own contributions: STP & HAMPI

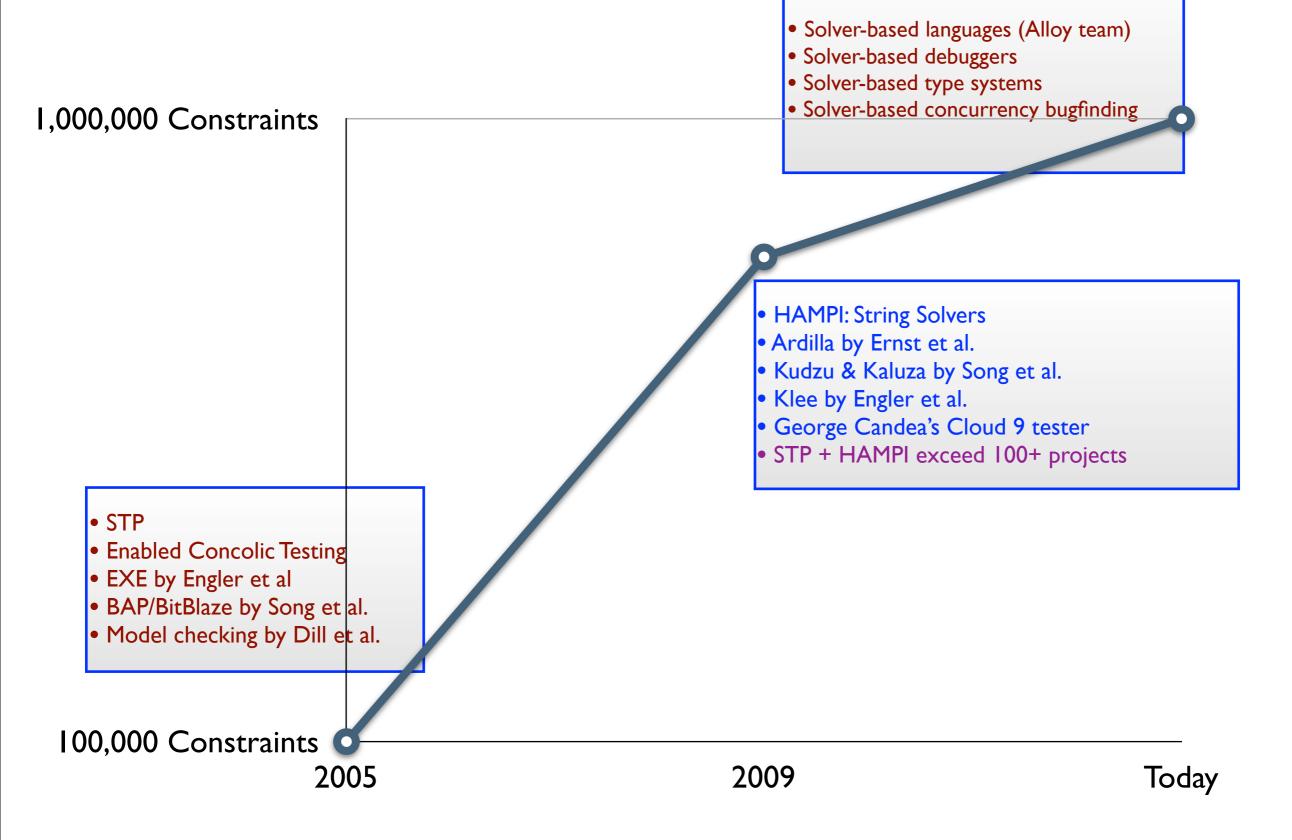
- STP: Abstraction-refinement for solving
- Applications to dynamic symbolic testing (aka concolic testing)
- HAMPI: Bounded logics
- SAT/SMT-based applications
- Future of SAT/SMT solvers

### STP Bit-vector & Array Solver



- Bit-vector or machine arithmetic
- Arrays for memory
- C/C++/Java expressions
- NP-complete

### The History of STP



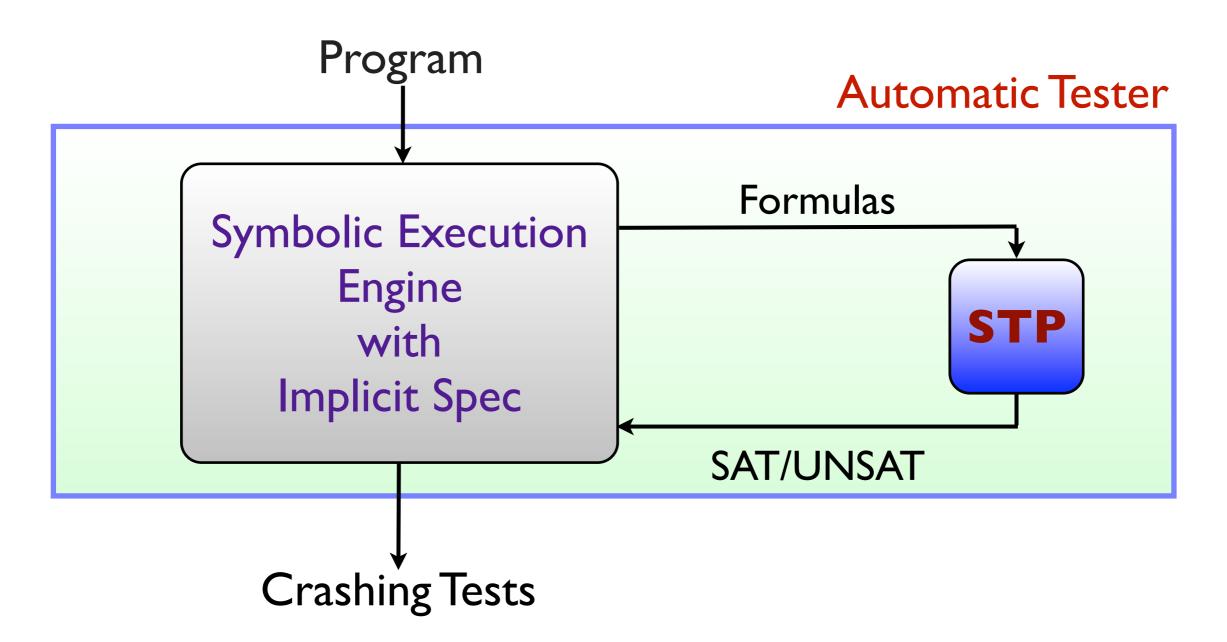
# Programs Reasoning & STP Why Bit-vectors and Arrays

- STP logic tailored for software reliability applications
- Support symbolic execution/program analysis

C/C++/Java/	Bit-vectors and Arrays
Int Var Char Var	32 bit variable 8 bit variable
Arithmetic operation $(x+y, x-y, x^*y, x/y,)$	Arithmetic function (x+y,x-y,x*y,x/y,)
assignments x = expr;	equality x = expr;
if conditional if(cond) $x = expr^1$ else $x = expr^2$	if-then-else construct $x = if(cond) expr^1 else expr^2$
inequality	inequality predicate
Memory read/write x = *ptr + i;	Array read/write ptr[]; x = Read(ptr,i);
Structure/Class	Serialized bit-vector expressions
Function	Symbolic execution
Loops	Bounding

### How to Automatically Crash Programs? Concolic Execution & STP

Problem: Automatically generate crashing tests given only the code



Structured input processing code: PDF Reader, Movie Player,...

```
Buggy_C_Program(int* data_field, int len_field) {
  int * ptr = malloc(len_field*sizeof(int));
  int i; //uninitialized

  while (i++ < process(len_field)) {
    //I. Integer overflow causing NULL deref
    //2. Buffer overflow
    *(ptr+i) = process_data(*(data_field+i));
  }
}</pre>
```

- Formula captures computation
- Tester attaches formula to capture spec

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    *(ptr+i) = process_data(*(data_field+i));
  }
}</pre>
```

Equivalent Logic Formula derived using symbolic execution

```
data_field, mem_ptr : ARRAY;
len_field : BITVECTOR(32); // symbolic
i, j, ptr : BITVECTOR(32); // symbolic
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+I] = process_data(data_field[i+I]);
.
.
```

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len_field : BITVECTOR(32); //symbolic
i, j, ptr : BITVECTOR(32); //symbolic
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+1] = process_data(data_field[i+1]);
.
.
```

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Structured input processing code: PDF Reader, Movie Player,...

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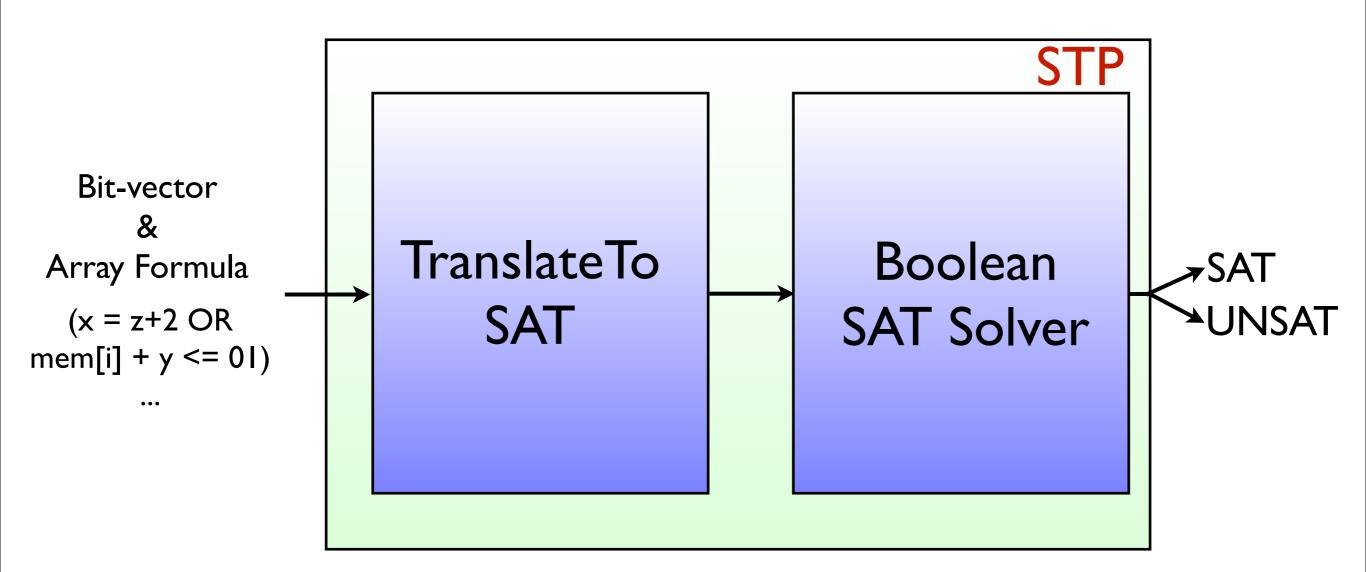
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Equivalent Logic Formula derived using symbolic execution

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data_field, mem_ptr : ARRAY;
len_field : BITVECTOR(32); // symbolic
i, j, ptr : BITVECTOR(32); // symbolic
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+I] = process_data(data_field[i+I]);
.
.
//INTEGER OVERFLOW QUERY
0 <= j <= process(len_field);
ptr + i + j = 0?</pre>
```

- Formula captures computation
- Tester attaches formula to capture spec

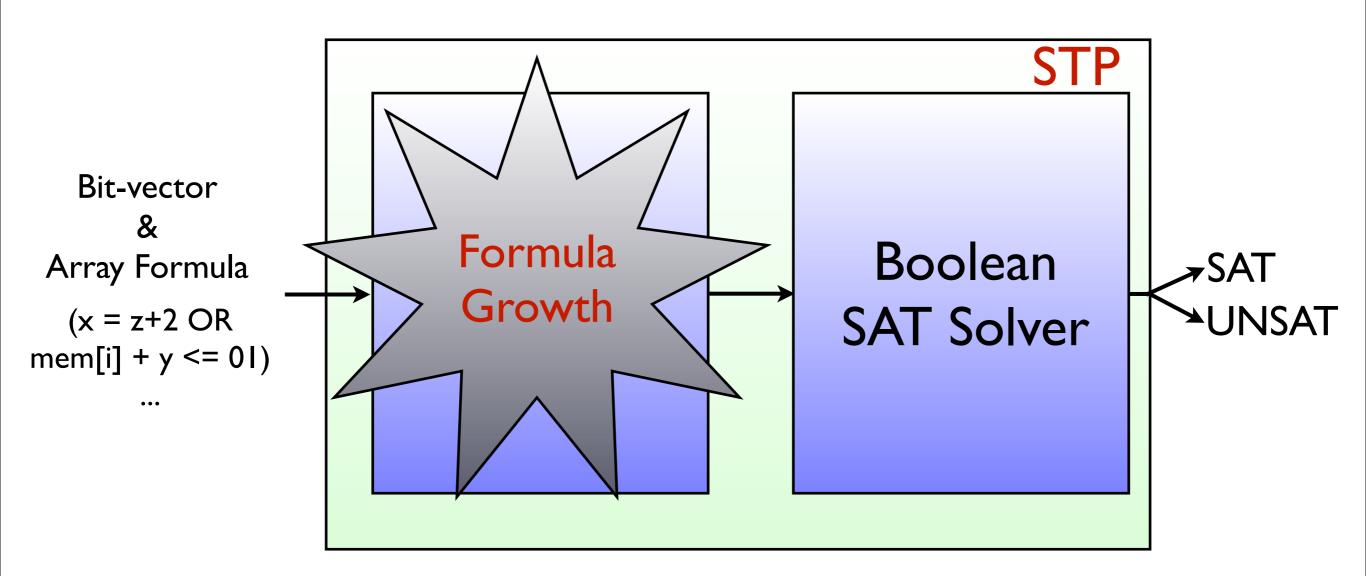
# How STP Works Bird's Eye View: Translate to SAT



### Why Translate to SAT?

- Both theories NP-complete
- Non SAT approaches didn't work
- Translation to SAT leverages solid engineering

# How STP Works Rich Theories cause MEM Blow-up



- Making information explicit
  - Space cost
  - Time cost

# Explicit Information causes Blow-up Array Memory Read Problem

Logic Formula derived using symbolic execution

```
data_field, mem_ptr : ARRAY;
len_field : BITVECTOR(32); //symbolic
i, j, ptr : BITVECTOR(32); //symbolic
.
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+1] = process_data(data_field[i+1]);
.
.
if(ptr+i = ptr+j) then mem_ptr[ptr+i] = mem_ptr[ptr+j);

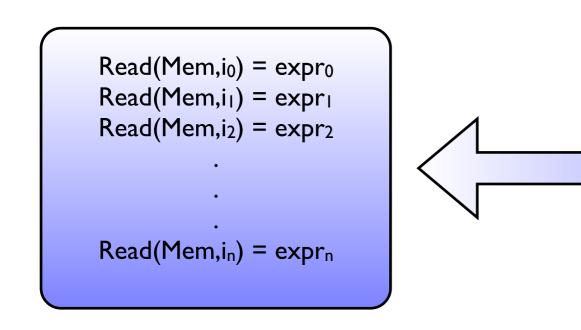
//INTEGER OVERFLOW QUERY
0 <= j <= process(len_field);
ptr + i + j < ptr?</pre>
```

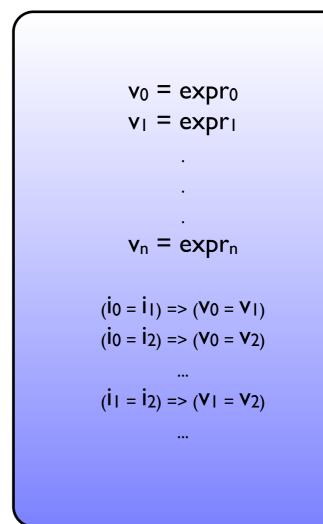
- Array Aliasing is implicit
- Need to make information explicit during solving
- Cannot be avoided

### How STP Works Array-read MEM Blow-up Problem

- Problem: O(n2) axioms added, n is number of read indices
- Lethal, if n is large, say, n = 100,000; # of axioms is 10 Billion

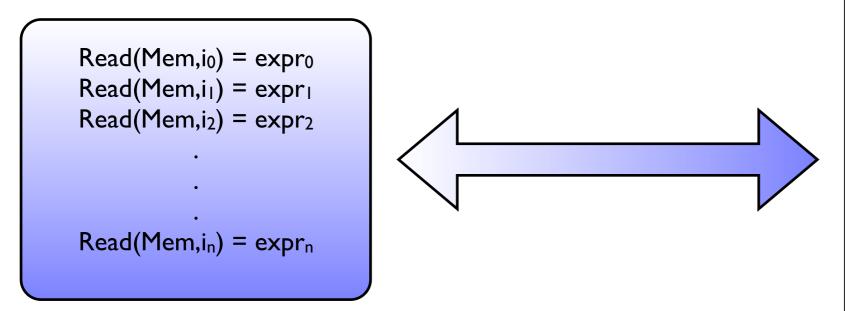
### Formula Growth





# How STP Works The Array-read Solution

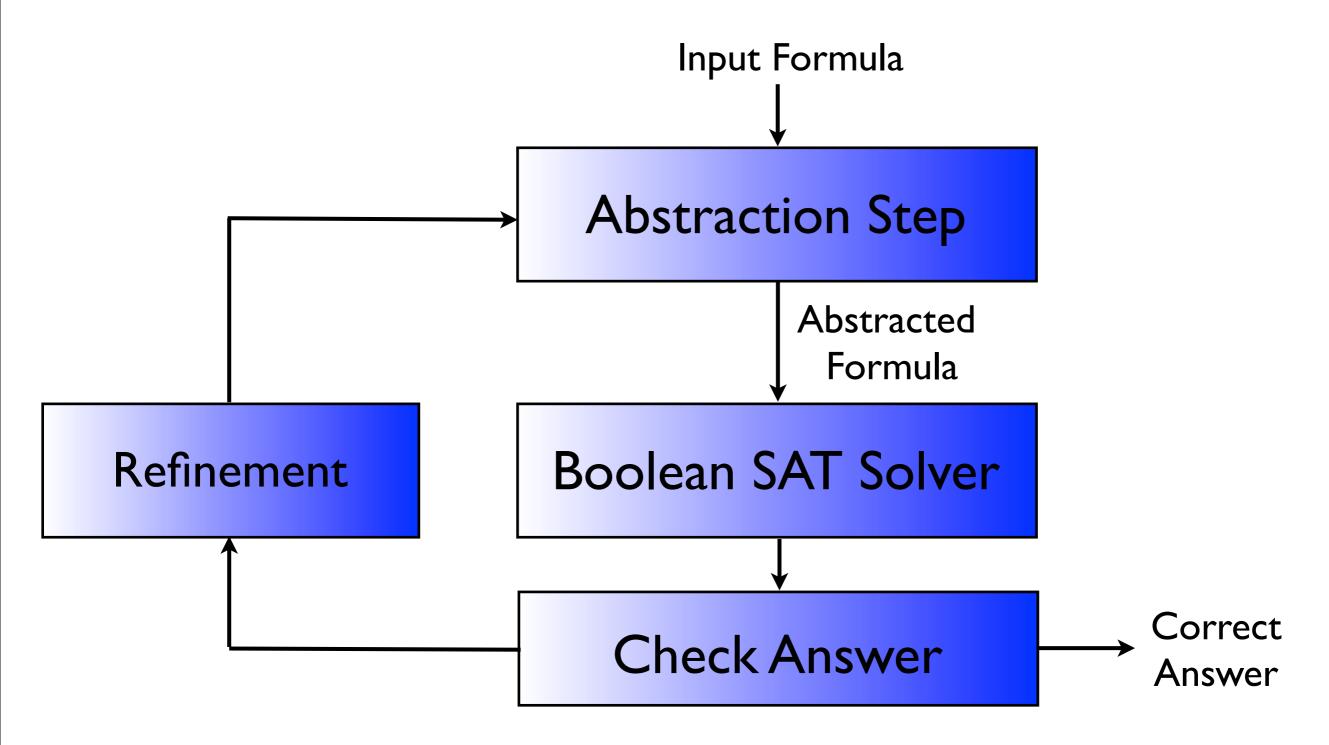
- Key Observation
  - Most indices don't alias in practice
  - Exploit locality of memory access in typical programs
  - Need only a fraction of array axioms for equivalence



```
v<sub>0</sub> = expr<sub>0</sub>
v<sub>1</sub> = expr<sub>1</sub>
.
.
.
.
.
.
.
v<sub>n</sub> = expr<sub>n</sub>

(io = i<sub>1</sub>) => (Vo = V<sub>1</sub>)
```

# STP Key Conceptual Contribution Abstraction-refinement Principle

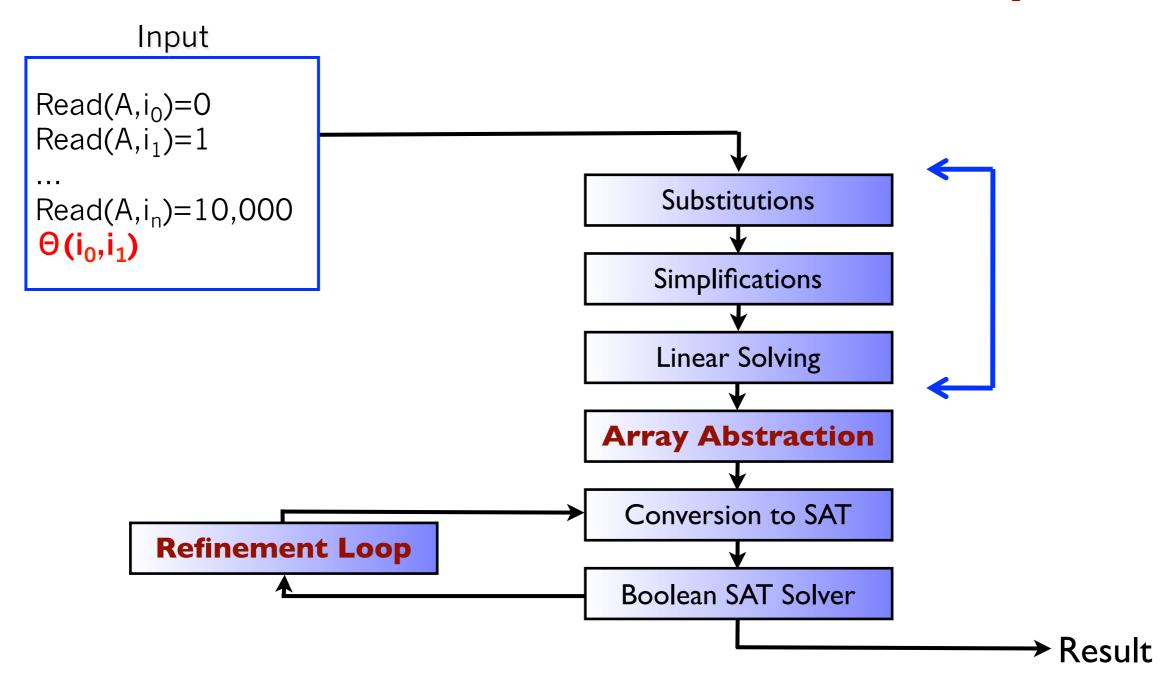


### How STP Works What to Abstract & How to Refine?

Abstraction	Refinement
<ul><li>I. Less essential parts</li><li>2. Causes MEM blow-up</li></ul>	I. Guided 2. Must remember
Abstraction manages formula growth hardness	Refinement manages search-space hardness

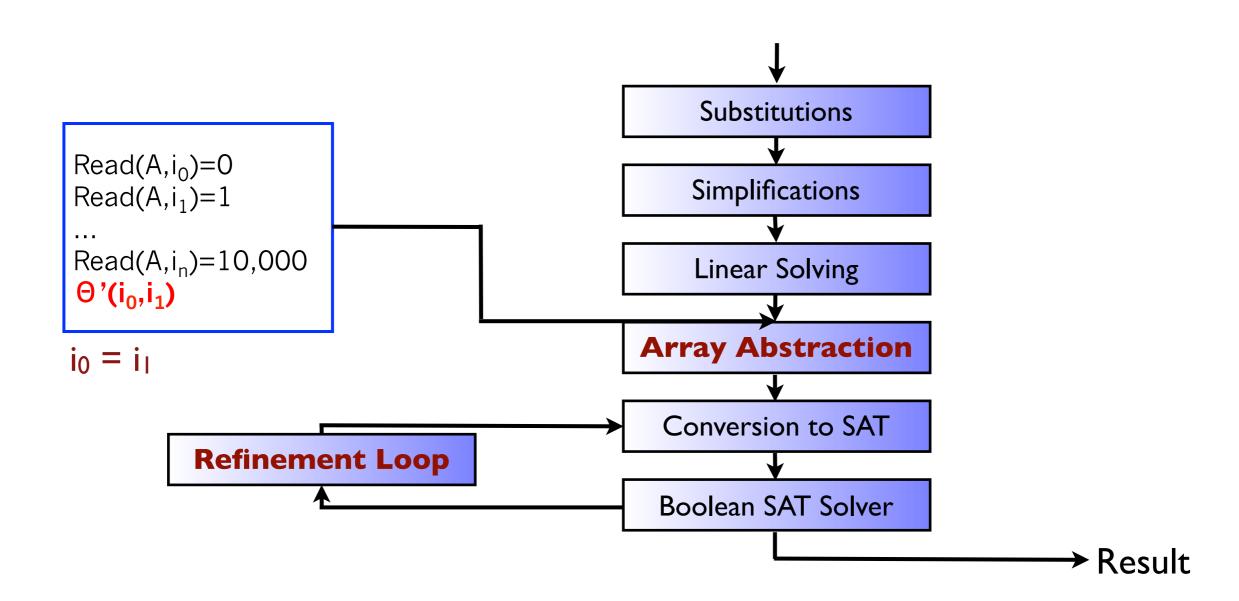
### How STP Works

### Abstraction-refinement for Array-reads

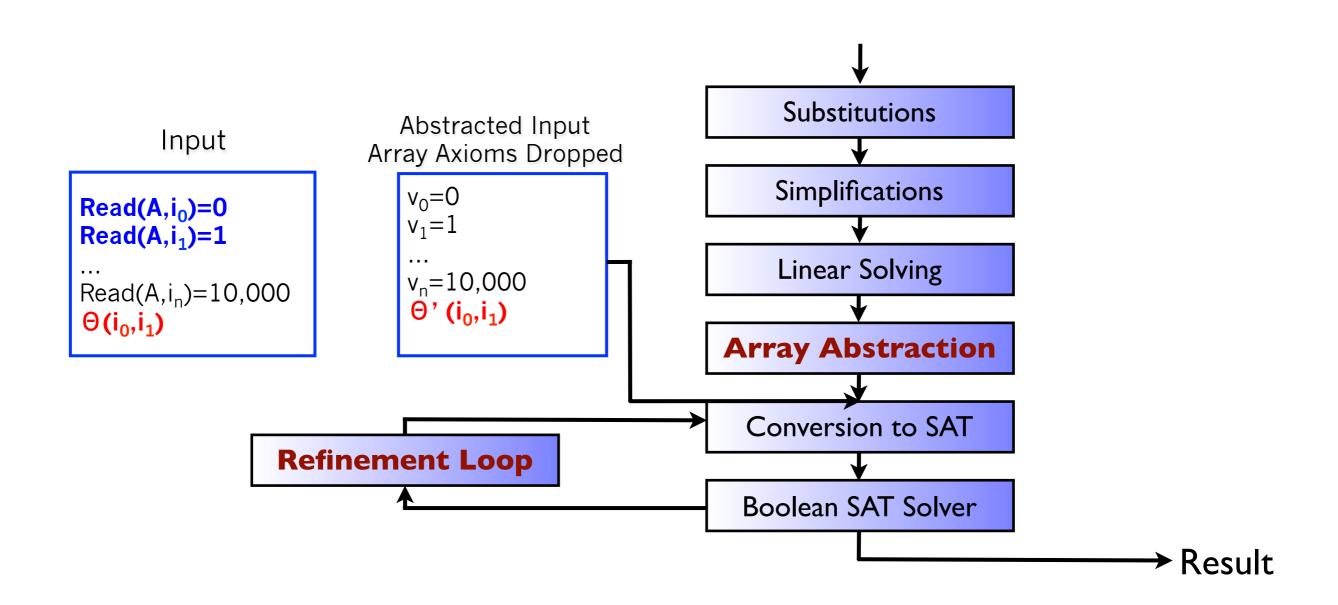


### How STP Works

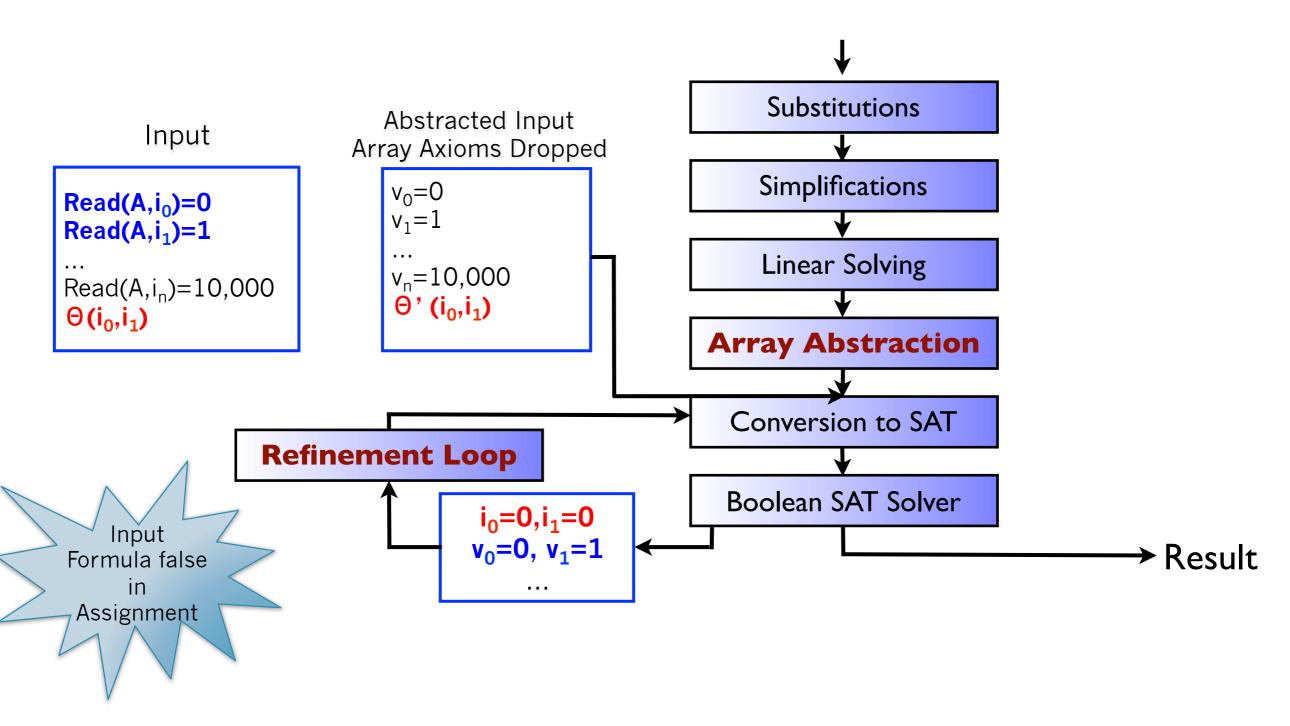
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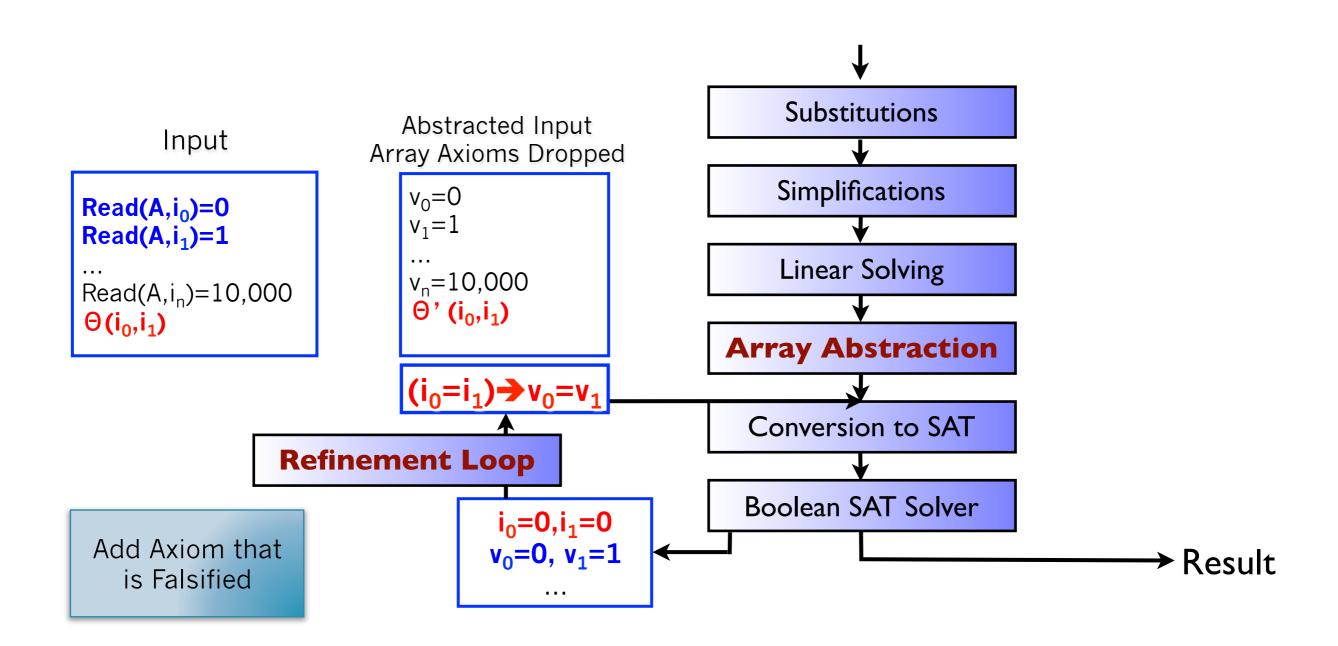
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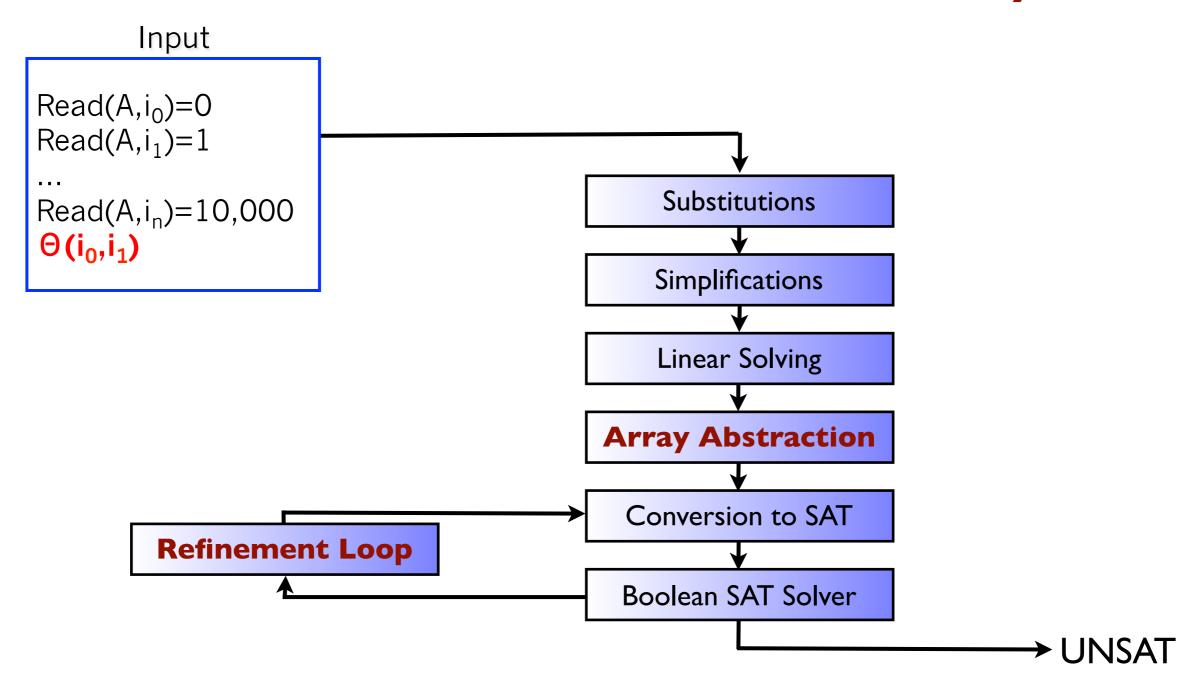
#### Abstraction-refinement for Array-reads



#### Abstraction-refinement for Array-reads



#### Abstraction-refinement for Array-reads



#### STP vs. Other Solvers

Testcase (Formula Size)	Result	Z3 (sec)	Yices (sec)	STP(s ec)
610dd9c (~15K)	SAT	TimeOut	MemOut	37
Grep65 (~60K)	UNSAT	0.3	TimeOut	4
Grep84 (~69K)	SAT	176	TimeOut	18
Grep106 (~69K)	SAT	130	TimeOut	227
Blaster4 (~262K)	UNSAT	MemOut	MemOut	10
Testcase20 (~1.2M)	SAT	MemOut	MemOut	56
Testcase21 (~1.2M)	SAT	MemOut	MemOut	43

<sup>\*</sup> All experiments on 3.2 GHz, 512 Kb cache

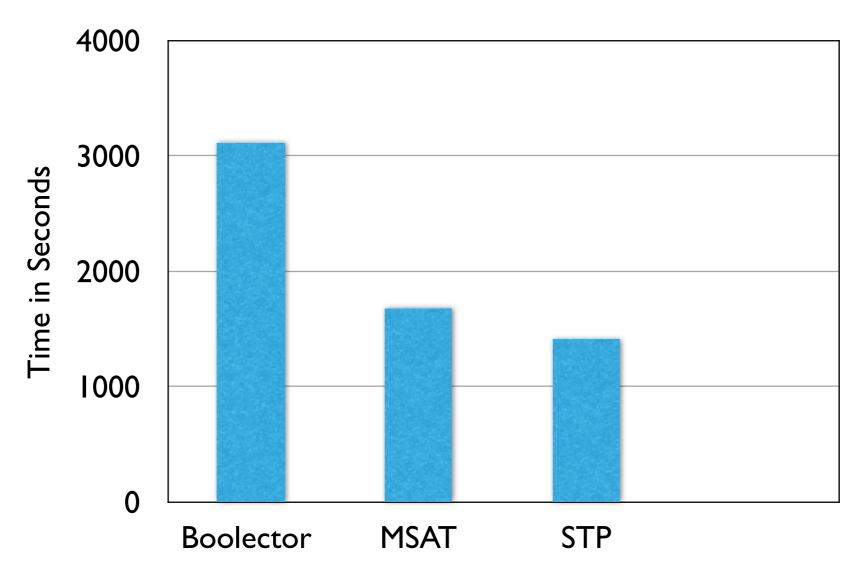
<sup>\*</sup> MemOut: 3.2 GB (Memory used by STP much smaller), TimeOut: 1800 seconds

<sup>\*</sup> Examples obtained from Dawn Song at Berkeley, David Molnar at Berkeley and Dawson Engler at Stanford

<sup>\*</sup> Experiments conducted in 2007

## STP vs. Other Leading Solvers

STP vs. Boolector & MathSAT on 615 SMTCOMP 2007 - 2010 examples



<sup>\*</sup> All experiments on 2.4 GHz, I GB RAM

<sup>\*</sup>Timeout: 500 seconds/example

## Impact of STP

- Enabled existing SE technologies to scale
  - Bounded model checkers, e.g., Chang and Dill

- Easier to engineer SE technologies
  - Formal tools (ACL2+STP) for verifying Crypto, Smith & Dill

- Enabled new SE technologies
  - Concolic testing (EXE,Klee,...) by Engler et al., Binary Analysis by Song et al.

## mpact of STP: Notable Projects

- Enabled Concolic Testing
- 100+ reliability and security projects

Category	Research Project	Project Leader/Institution
Formal Methods	ACL2 Theorem Prover + STP Verification-aware Design Checker Java PathFinder Model Checker	Eric Smith & David Dill/Stanford Jacob Chang & David Dill/Stanford Mehlitz & Pasareanu/NASA
Program Analysis	BitBlaze & WebBlaze BAP	Dawn Song et al./Berkeley David Brumley/CMU
Automatic Testing Security	Klee, EXE SmartFuzz Kudzu	Engler & Cadar/Stanford Molnar & Wagner/Berkeley Saxena & Song/Berkeley
Hardware Bounded Model-cheking (BMC)	Blue-spec BMC BMC	Katelman & Dave/MIT Haimed/NVIDIA

# Impact of STP http://www.metafuzz.com

<b>Program Name</b>	Lines of Code	Number of Bugs Found	<u>Team</u>
Mplayer	~900,000	Hundreds	David Molnar/Berkeley & Microsoft Research
Evince	~90,000	Hundreds	David Molnar/Berkeley & Microsoft Research
Unix Utilities	1000s	Dozens	Dawson Engler et al./Stanford
Crypto Hash Implementations	1000s	Verified	Eric Smith & David Dill/Stanford

#### Rest of the Talk

#### STP Bit-vector and Array Solver

- Why Bit-vectors and Arrays?
- How does STP scale: Abstraction-refinement
- Impact: Concolic testing
- Experimental Results

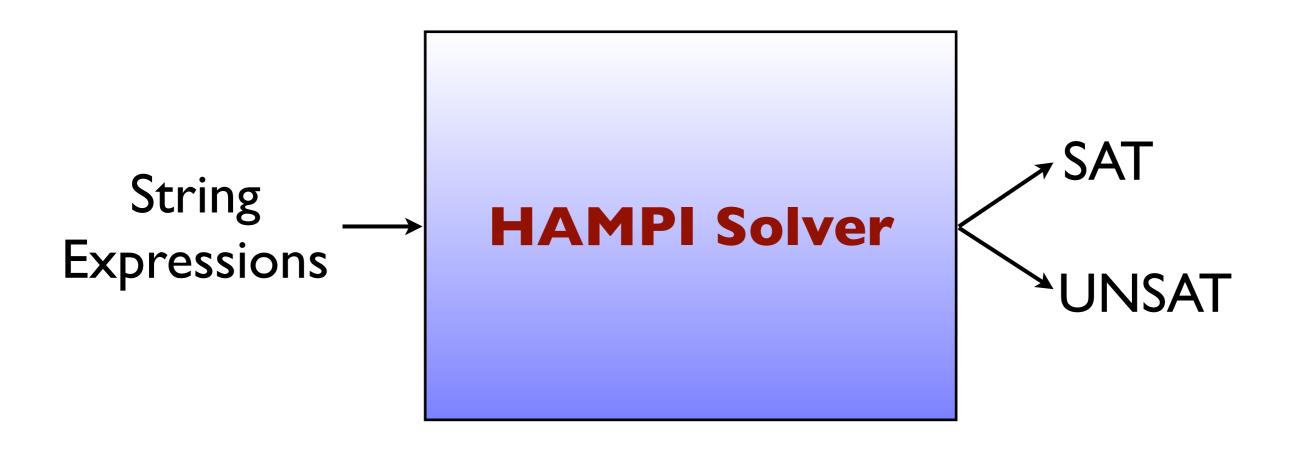
#### HAMPI String Solver

- Why Strings?How does HAMPI scale: Bounding
- Impact: String-based program analysis
- Experimental Results

#### Future Work

- Multicore SAT
- SAT-based Languages

## HAMPI String Solver



- $X = concat("SELECT...",v) AND (X \in SQL_grammar)$
- JavaScript and PHP Expressions
- Web applications, SQL queries
- NP-complete

# Theory of Strings The Hampi Language

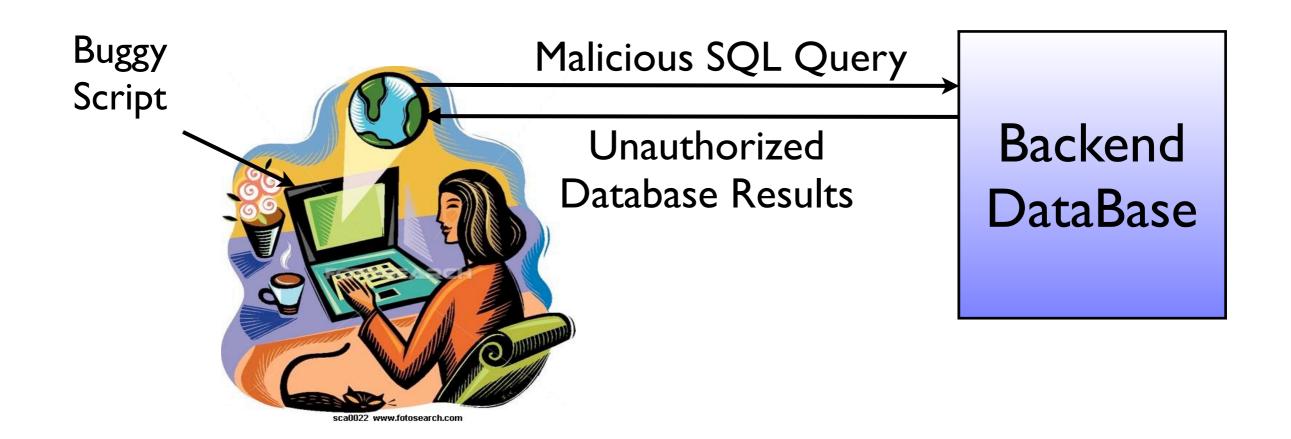
PHP/JavaScript/C++	HAMPI: Theory of Strings	<u>Notes</u>
Var a; \$a = 'name'	Var a : I20; a = 'name'	Bounded String Variables String Constants
string_expr." is "	concat(string_expr," is ");	Concat Function
substr(string_expr, 1,3)	string_expr[1:3]	Extract Function
<pre>assignments/strcmp a = string_expr; a /= string_expr;</pre>	equality a = string_expr; a /= string_expr;	Equality Predicate
Sanity check in regular expression RE Sanity check in context-free grammar CFG	string_expr in RE string_expr in SQL string_expr NOT in SQL	Membership Predicate
string_expr contains a sub_str string_expr does not contain a sub_str	string_expr contains sub_str string_expr NOT?contains sub_str	Contains Predicate (Substring Predicate)

# Theory of Strings The Hampi Language

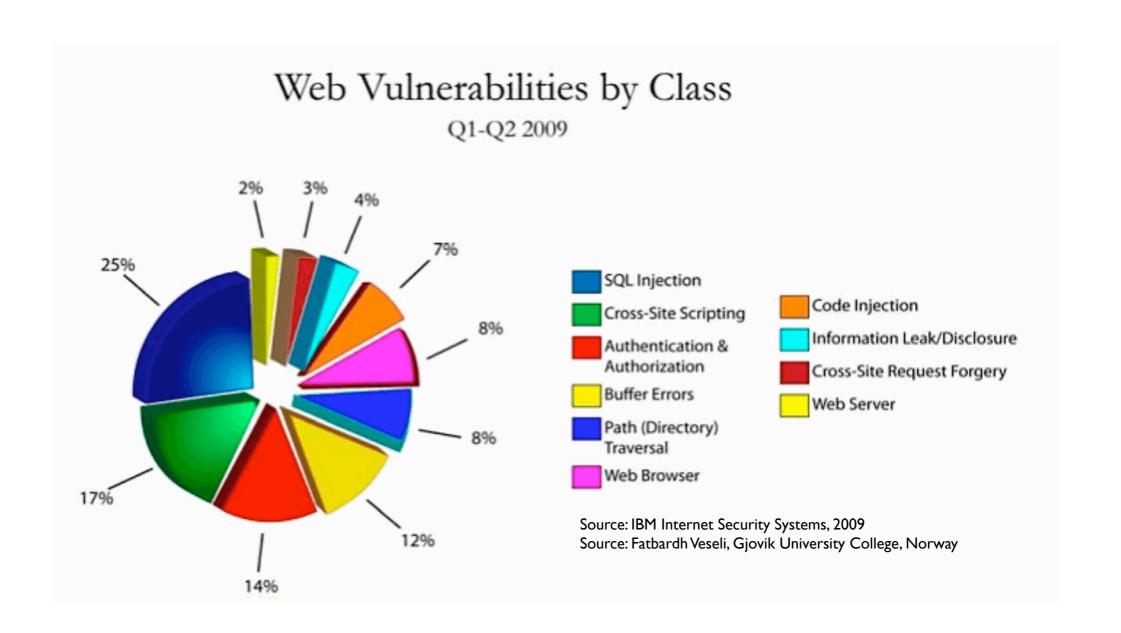
X = concat("SELECT msg FROM msgs WHERE topicid = ",v)
 AND
 (X ∈ SQL\_Grammar)

input  $\in \text{RegExp}([0-9]+)$ 

X = concat (str\_term1, str\_term2, "c")[1:42]
 AND
 X contains "abc"



SELECT m FROM messages WHERE id='I' OR I = I



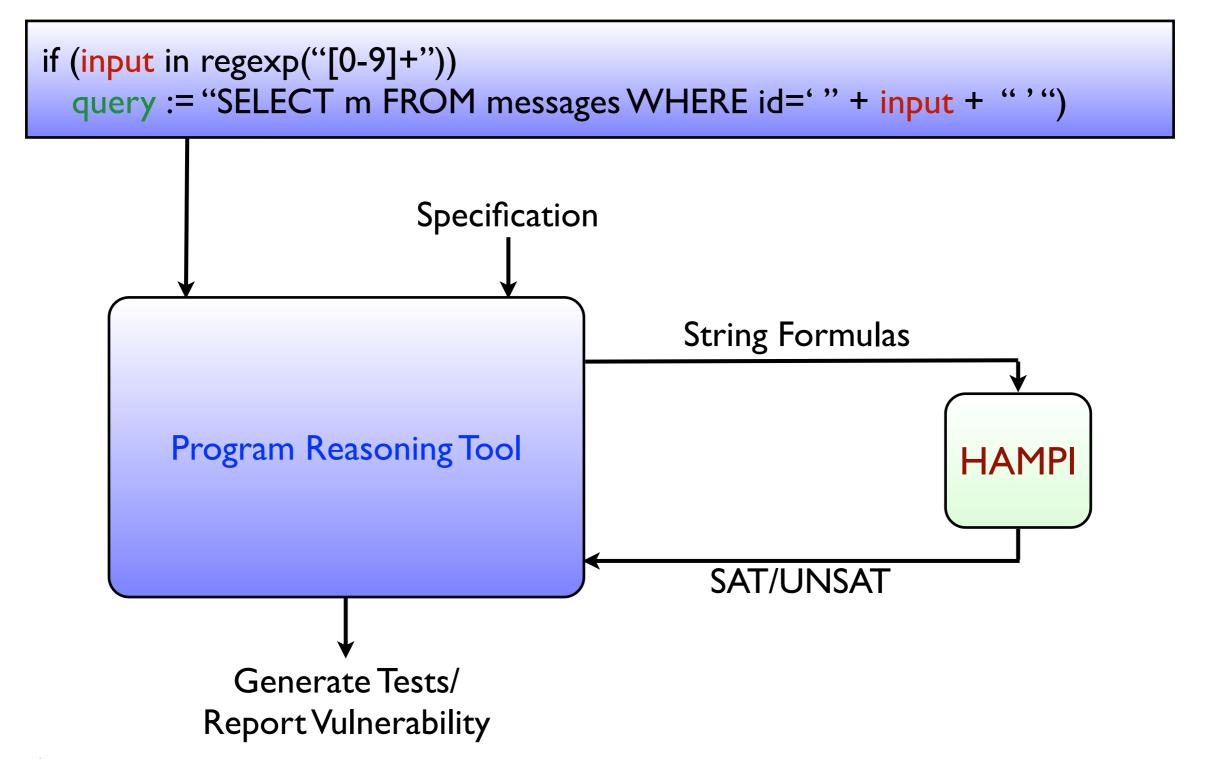
**Buggy Script** 

```
if (input in regexp("[0-9]+"))
query := "SELECT m FROM messages WHERE id="" + input + " '")
```

- input passes validation (regular expression check)
- query is syntactically-valid SQL
- query can potentially contain an attack substring (e.g., I' OR 'I' = 'I)

```
if (input in regexp("[0-9]+"))
query := "SELECT m FROM messages WHERE id="" + input + """)
```

- input passes validation (regular expression check)
- query is syntactically-valid SQL
- query can potentially contain an attack substring (e.g., I' OR 'I' = 'I)



### Rest of the Talk

- HAMPI Logic: A Theory of Strings
- Motivating Example: HAMPI-based Vulnerability Detection App
- How HAMPI works
- Experimental Results
- Related Work: Theory and Practice
- HAMPI 2.0
- SMTization: Future of Strings

# Expressing the Problem in HAMPI SQL Injection Vulnerabilities

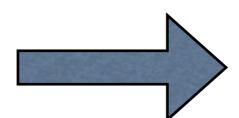
```
Input String | Var v : 12;
                  cfg SqlSmall := "SELECT" [a-z]+ "FROM" [a-z]+ "WHERE "Cond;
    SQL
                  cfg Cond := Val "=" Val | Cond " OR " Cond;
 Grammar
                  cfg Val := [a-z] + | """ [a-z0-9] * """ | [0-9] +;
 SQL Query
                  val q := concat("SELECT msg FROM messages WHERE topicid="", v, """);
                  assert v in [0-9]+;
                                                   "q is a valid SQL query"
                  assert q in SqlSmall;
 SQLI attack
 conditions
                  assert q contains "OR '1'='1";
                                                  "'q contains an attack vector"
```

## Hampi Key Conceptual Idea Bounding, expressiveness and efficiency

Li	Complexity of $\emptyset = L_1 \cap \cap L_n$	Current Solvers
Context-free	Undecidable	n/a
Regular	PSPACE-complete	Quantified Boolean Logic
Bounded	NP-complete	SAT Efficient in practice

## Hampi Key Idea: Bounded Logics Testing, Vulnerability Detection,...

- Finding SAT assignment is key
- •Short assignments are sufficient



- Bounding is sufficient
- Bounded logics easier to decide

## Hampi Key Idea: Bounded Logics Bounding vs. Completeness

Bounding leads to incompleteness

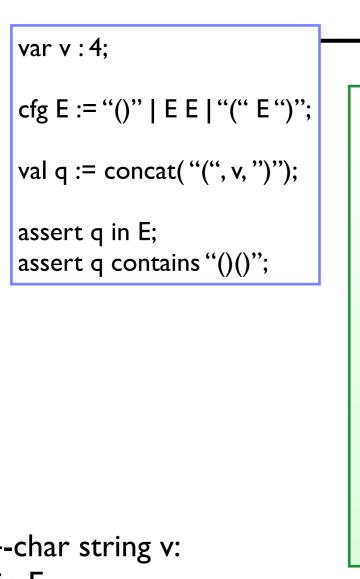
• Testing (Bounded MC) vs. Verification (MC)

• Bounding allows trade-off (Scalability vs. Completeness)

• Completeness (also, soundness) as resources

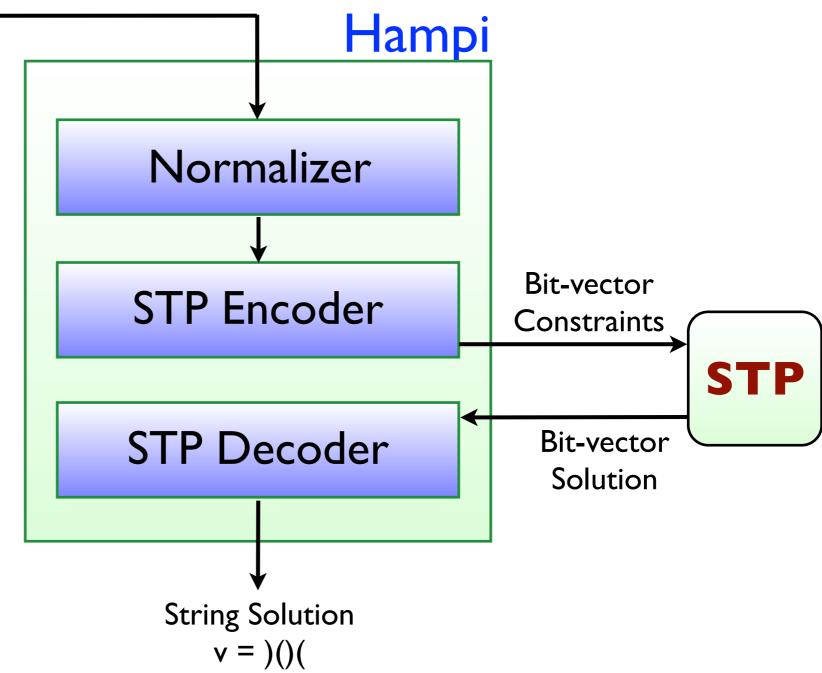
```
Input String | Var v : 12;
                    cfg SqlSmall := "SELECT" [a-z]+ "FROM" [a-z]+ "WHERE "Cond;
    SQL
                   cfg Cond := Val "=" Val | Cond " OR " Cond;
 Grammar
                    cfg Val := [a \cdot z] + | """ [a \cdot z \cdot 0 \cdot 9] * """ | [0 \cdot 9] +;
 SQL Query
                    val q := concat("SELECT msg FROM messages WHERE topicid="", v, """);
                   assert v in [0-9]+;
                                                       "q is a valid SQL query"
                    assert q in SqlSmall;
 SQLI attack
                    assert q contains "OR '1'='1";
  conditions
                                                     f "q contains an attack vector"
```

### How Hampi Works Bird's Eye View: Strings into Bit-vectors

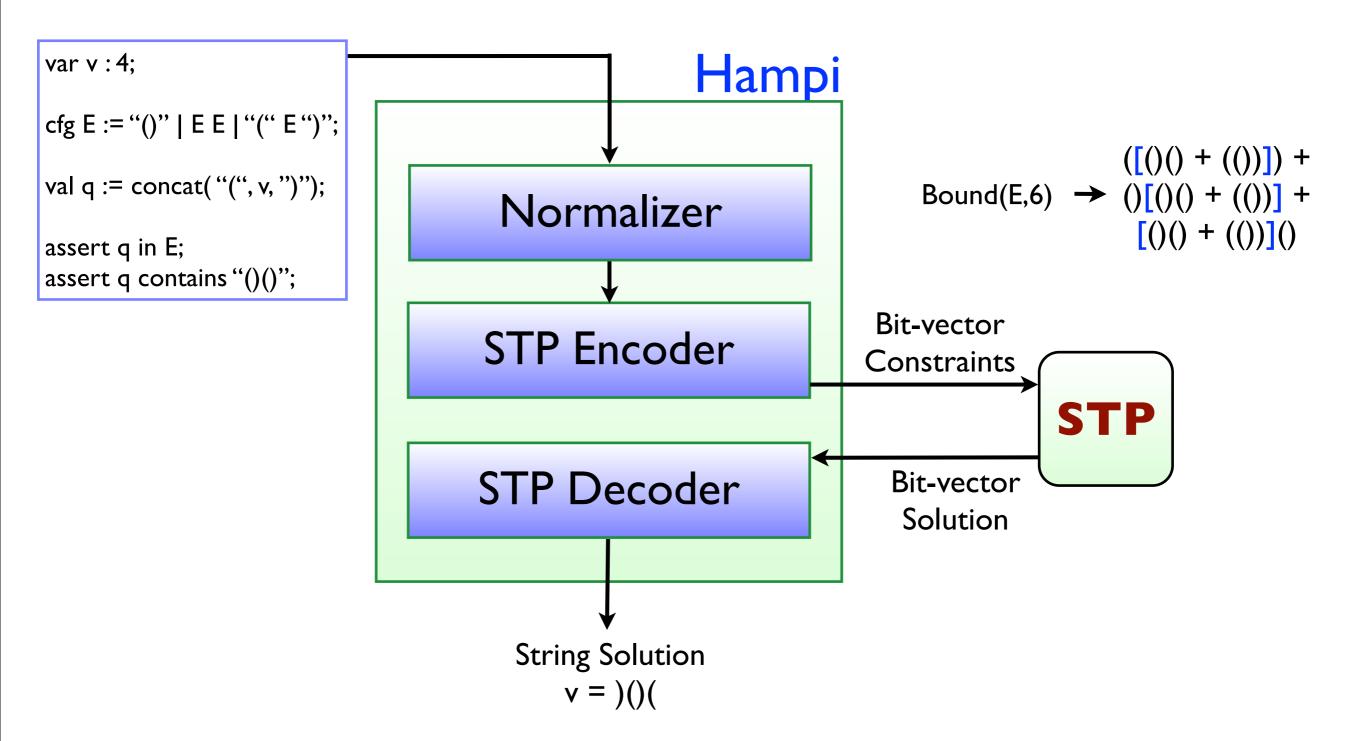


Find a 4-char string v:

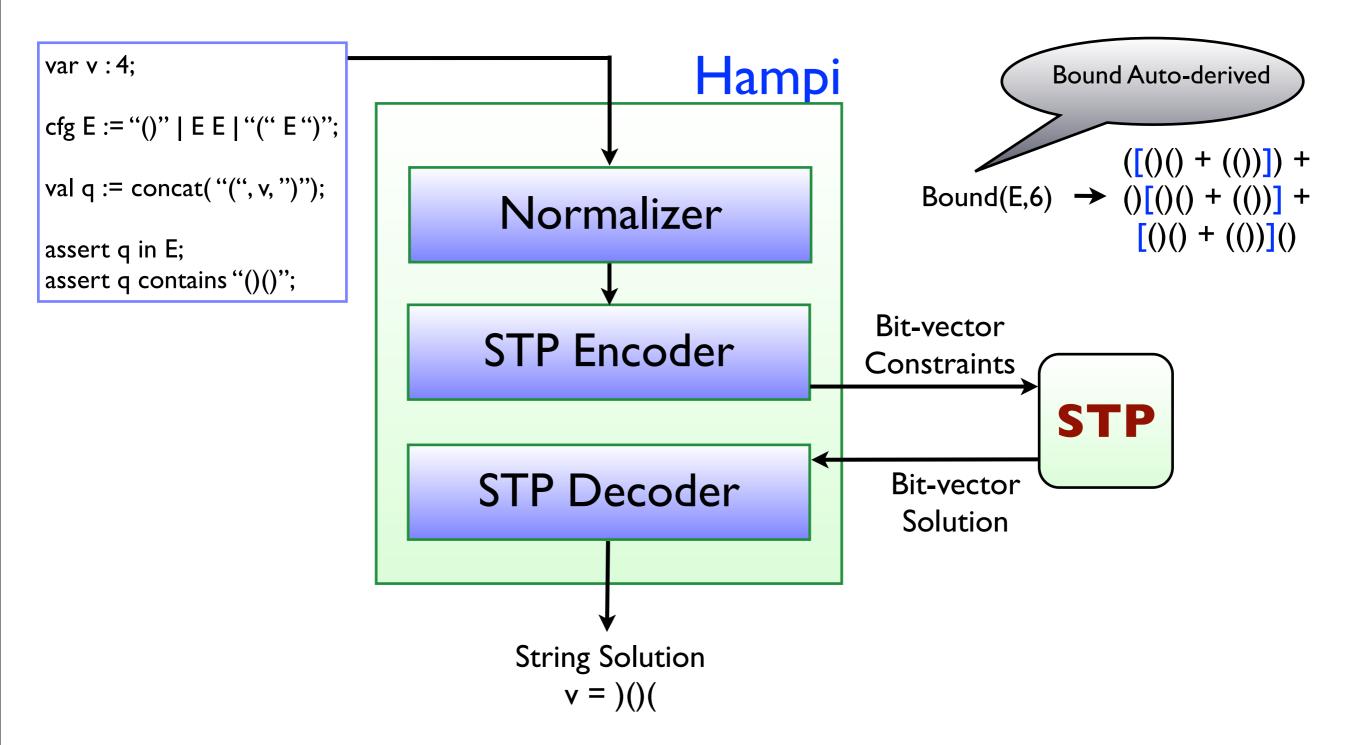
- (v) is in E
- (v) contains ()()



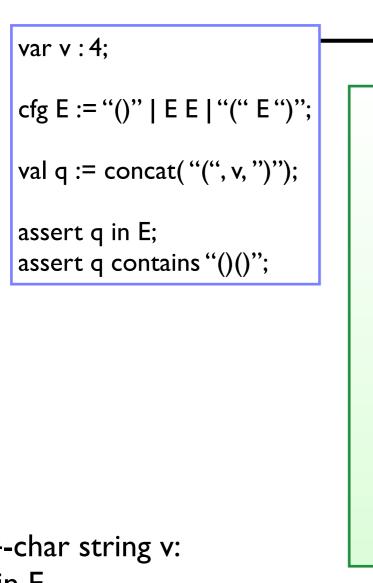
## How Hampi Works Unroll Bounded CFGs into Regular Exp.



## How Hampi Works Unroll Bounded CFGs into Regular Exp.

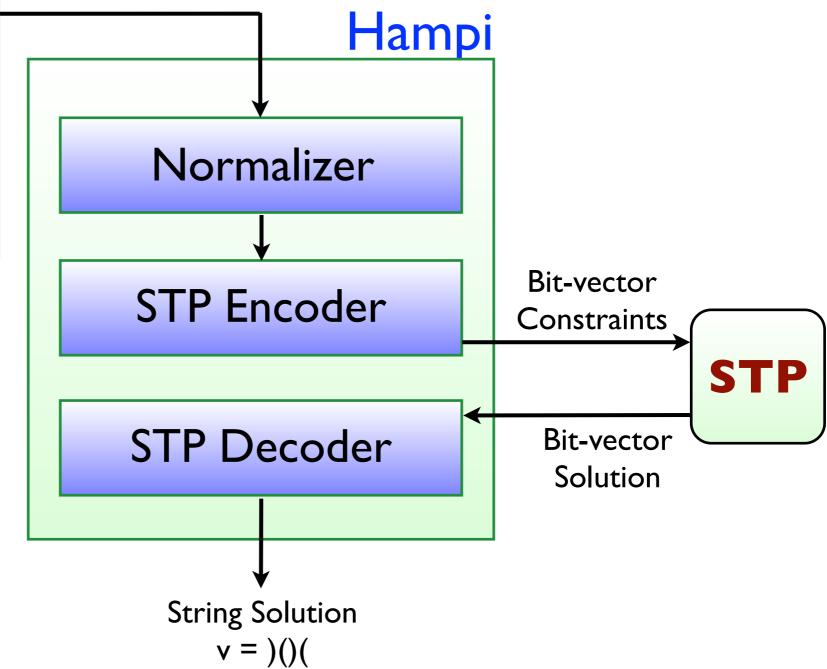


### How Hampi Works Bird's Eye View: Strings into Bit-vectors

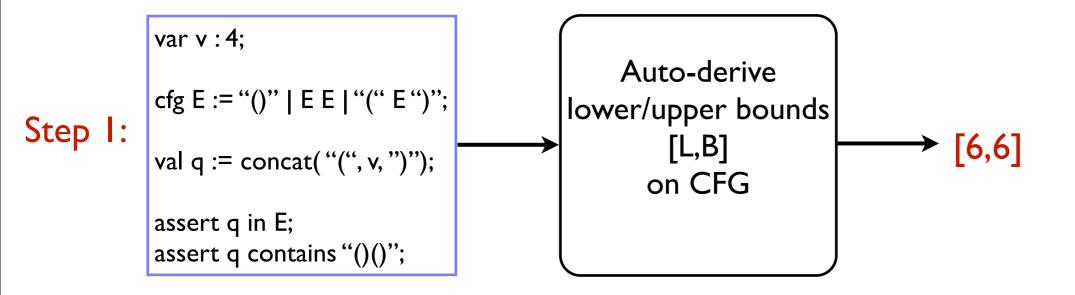


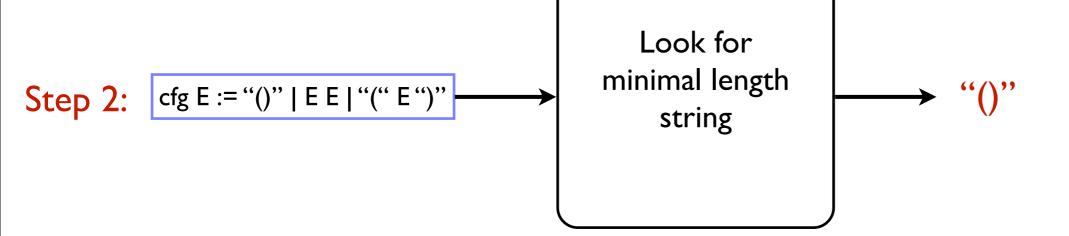
Find a 4-char string v:

- (v) is in E
- (v) contains ()()

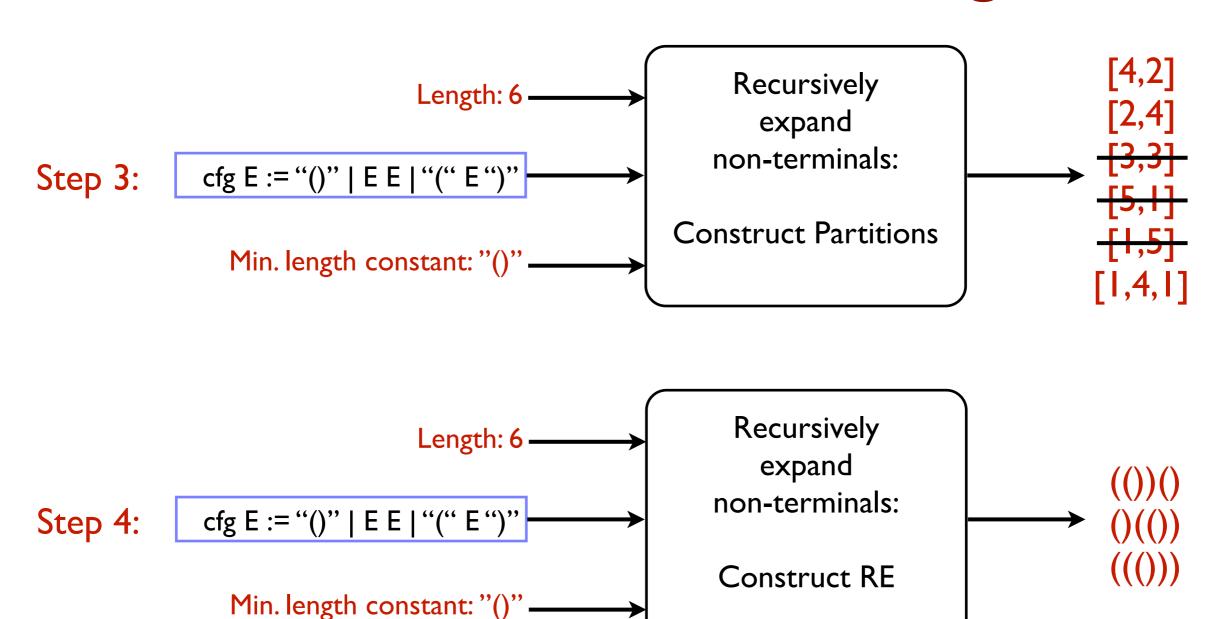


## How Hampi Works Unroll Bounded CFGs into Regular Exp.

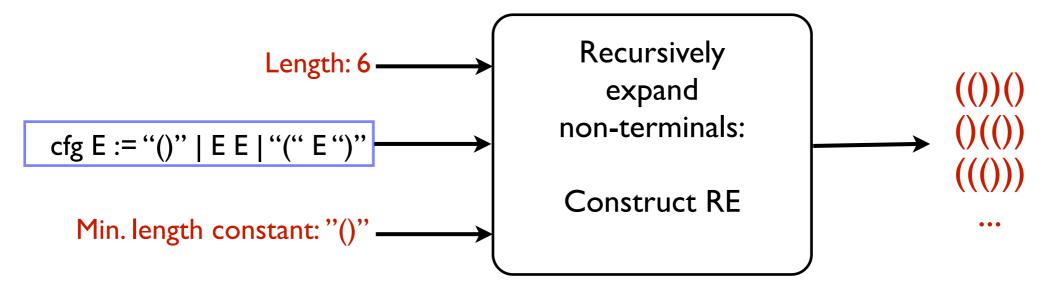




## How Hampi Works Unroll Bounded CFGs into Regular Exp.

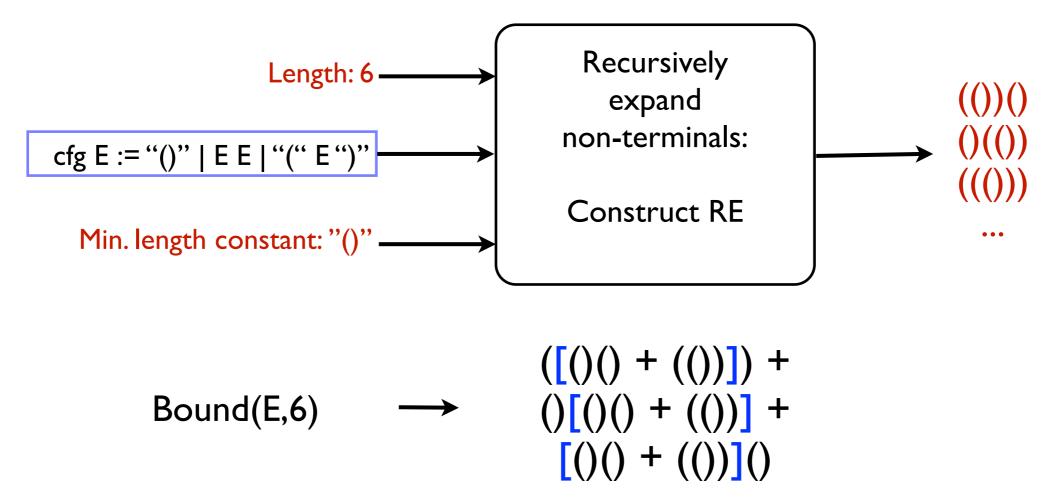


## Unroll Bounded CFGs into Regular Exp. Managing Exponential Blow-up



- Dynamic programming style
- Works well in practice

## Unroll Bounded CFGs into Regular Exp. Managing Exponential Blow-up



## How Hampi Works Converting Regular Exp. into Bit-vectors

Encode regular expressions recursively

```
Alphabet { (, ) } → 0, 1
constant → bit-vector constant
union + → disjunction ∨
concatenation → conjunction ∧
Kleene star * → conjunction ∧
```

Membership, equality → equality

```
 (v) \in ()[()() + (())] + [()() + (())]() + ([()() + (())]) 
 Formula \Phi_1 \quad V \quad Formula \Phi_2 \quad V \quad Formula \Phi_3 
 B[0] = 0 \land B[1] = 1 \land \{B[2] = 0 \land B[3] = 1 \land B[4] = 0 \land B[5] = 1 \quad V \dots
```

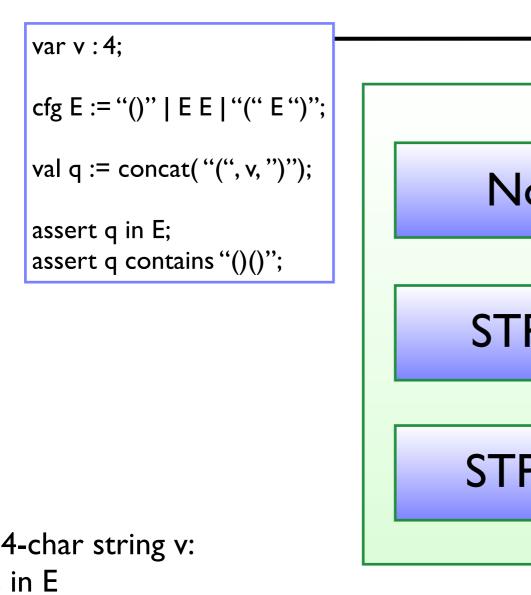
## How Hampi Works Converting Regular Exp. into Bit-vectors

Constraint Templates

Encode once, and reuse

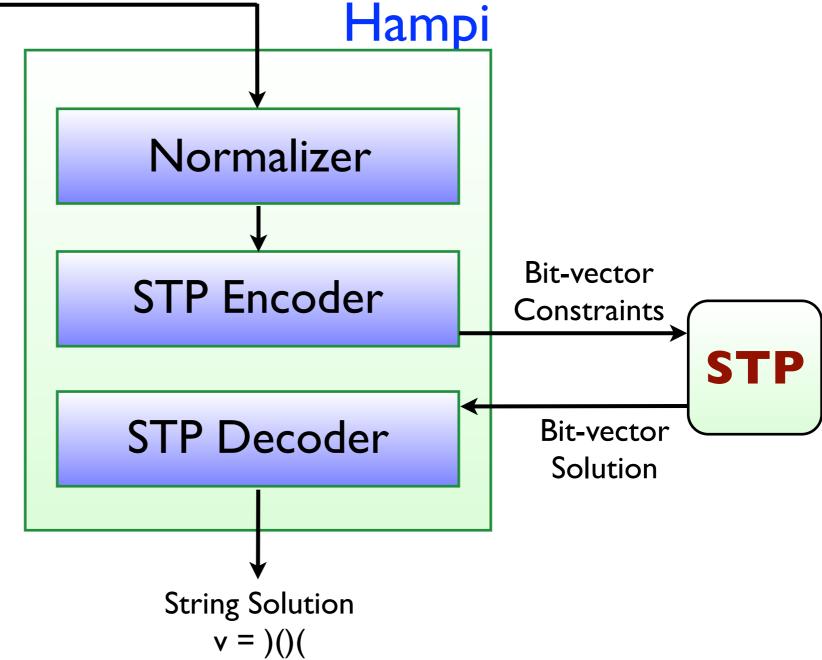
• On-demand formula generation

### How Hampi Works Decoder converts Bit-vectors to Strings



Find a 4-char string v:

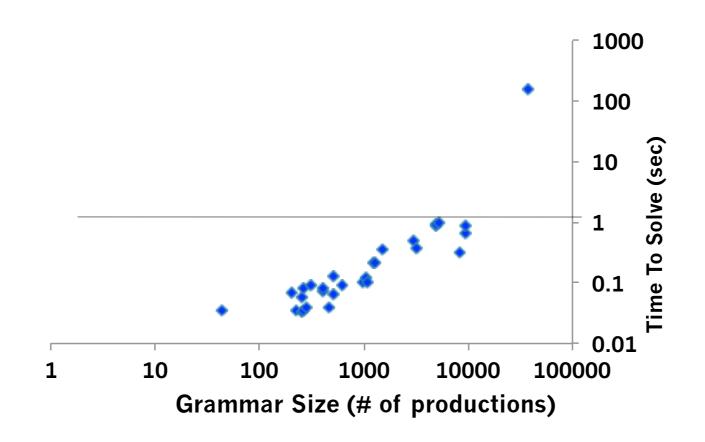
- (v) is in E
- (v) contains ()()



### Rest of the Talk

- HAMPI Logic: A Theory of Strings
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- Related Work: Theory and Practice
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# HAMPI: Result I Static SQL Injection Analysis



- 1367 string constraints from Wasserman & Su [PLDI'07]
- Hampi scales to large grammars
- Hampi solved 99.7% of constraints in < Isec
- All solvable constraints had short solutions

# HAMPI: Result 2 Security Testing and XSS

- Attackers inject client-side script into web pages
- Somehow circumvent same-origin policy in websites
- echo "Thank you \$my\_poster for using the message board";
- Unsanitized \$my\_poster
- Can be JavaScript
- Execution can be bad

# HAMPI: Result 2 Security Testing

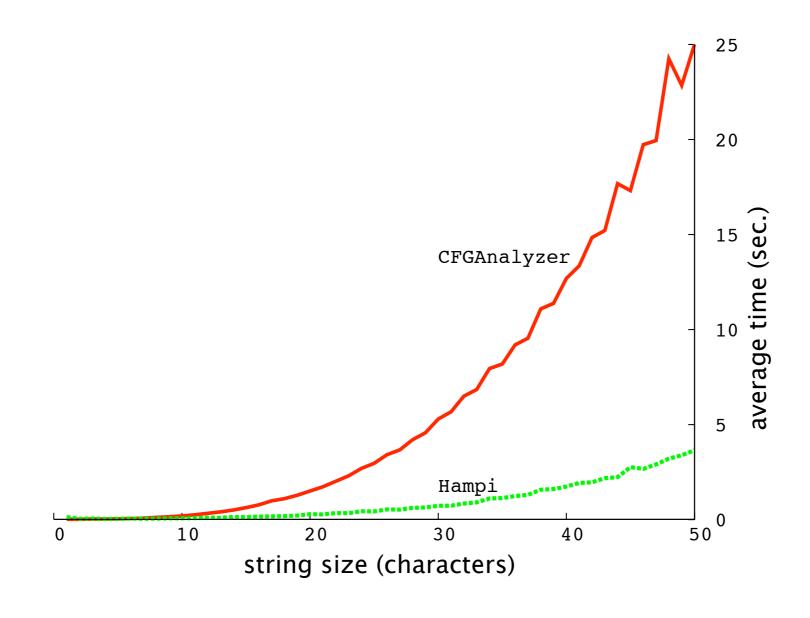
- Hampi used to build Ardilla security tester [Kiezun et al., ICSE'09]
- 60 new vulnerabilities on 5 PHP applications (300+ kLOC)
  - 23 SQL injection
  - 37 cross-site scripting (XSS)
    ←

5 added to
US National Vulnerability DB

- 46% of constraints solved in < I second per constraint
- 100% of constraints solved in <10 seconds per constraint

### HAMPI: Result 3

## Comparison with Competing Tools



• HAMPI vs. CFGAnalyzer (U. Munich): HAMPI ~7x faster for strings of size 50+

# HAMPI: Result 3 Comparison with Competing Tools

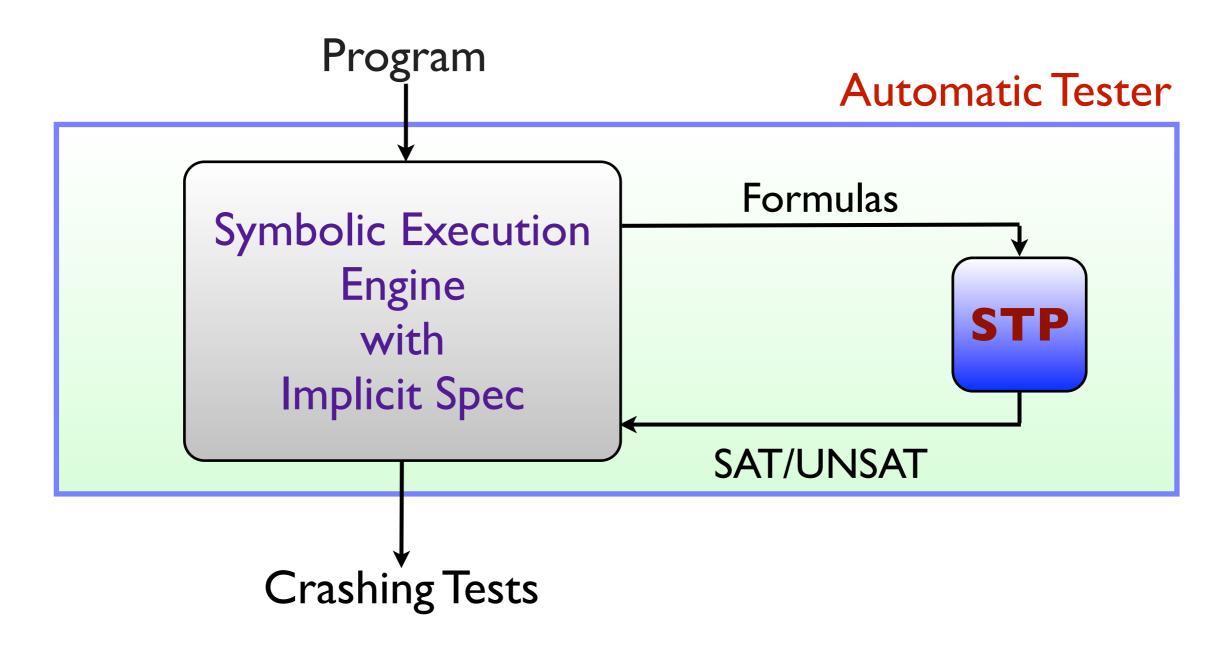
### RE intersection problems

• HAMPI 100x faster than Rex (MSR)

• HAMPI 1000x faster than DPRLE (U.Virginia)

• Pieter Hooimeijer 2010 paper titled 'Solving String Constraints Lazily'

Problem: Automatically generate crashing tests given only the code



Vijay Ganesh, Dagstuhl, Aug 8-12, 2011

Structured input processing code: PDF Reader, Movie Player,...

```
Buggy_C_Program(int* data_field, int len_field) {
  int * ptr = malloc(len_field*sizeof(int));
  int i; //uninitialized

  while (i++ < process(len_field)) {
    //I. Integer overflow causing NULL deref
    //2. Buffer overflow
    *(ptr+i) = process_data(*(data_field+i));
  }
}</pre>
```

- Formula captures computation
- Tester attaches formula to capture spec

Structured input processing code: PDF Reader, Movie Player,...

```
Buggy_C_Program(int* data_field, int len_field) {
  int * ptr = malloc(len_field*sizeof(int));
  int i; //uninitialized

  while (i++ < process(len_field)) {
    //I.Integer overflow causing NULL deref
    //2. Buffer overflow
    *(ptr+i) = process_data(*(data_field+i));
  }
}</pre>
```

Equivalent Logic Formula derived using symbolic execution

```
data_field, mem_ptr : ARRAY;
len_field : BITVECTOR(32); // symbolic
i, j, ptr : BITVECTOR(32); // symbolic
.
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+1] = process_data(data_field[i+1]);
.
.
```

- Formula captures computation
- Tester attaches formula to capture spec

Structured input processing code: PDF Reader, Movie Player,...

```
Buggy_C_Program(int* data_field, int len_field) {
  int * ptr = malloc(len_field*sizeof(int));
  int i; //uninitialized

  while (i++ < process(len_field)) {
    //I. Integer overflow causing NULL deref
    //2. Buffer overflow
    *(ptr+i) = process_data(*(data_field+i));
  }
}</pre>
```

Equivalent Logic Formula derived using symbolic execution

```
data_field, mem_ptr : ARRAY;
len_field : BITVECTOR(32); // symbolic
i, j, ptr : BITVECTOR(32); // symbolic
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+I] = process_data(data_field[i+I]);
.
.
```

- Formula captures computation
- Tester attaches formula to capture spec

Structured input processing code: PDF Reader, Movie Player,...

```
Buggy_C_Program(int* data_field, int len_field) {
  int * ptr = malloc(len_field*sizeof(int));
  int i; //uninitialized

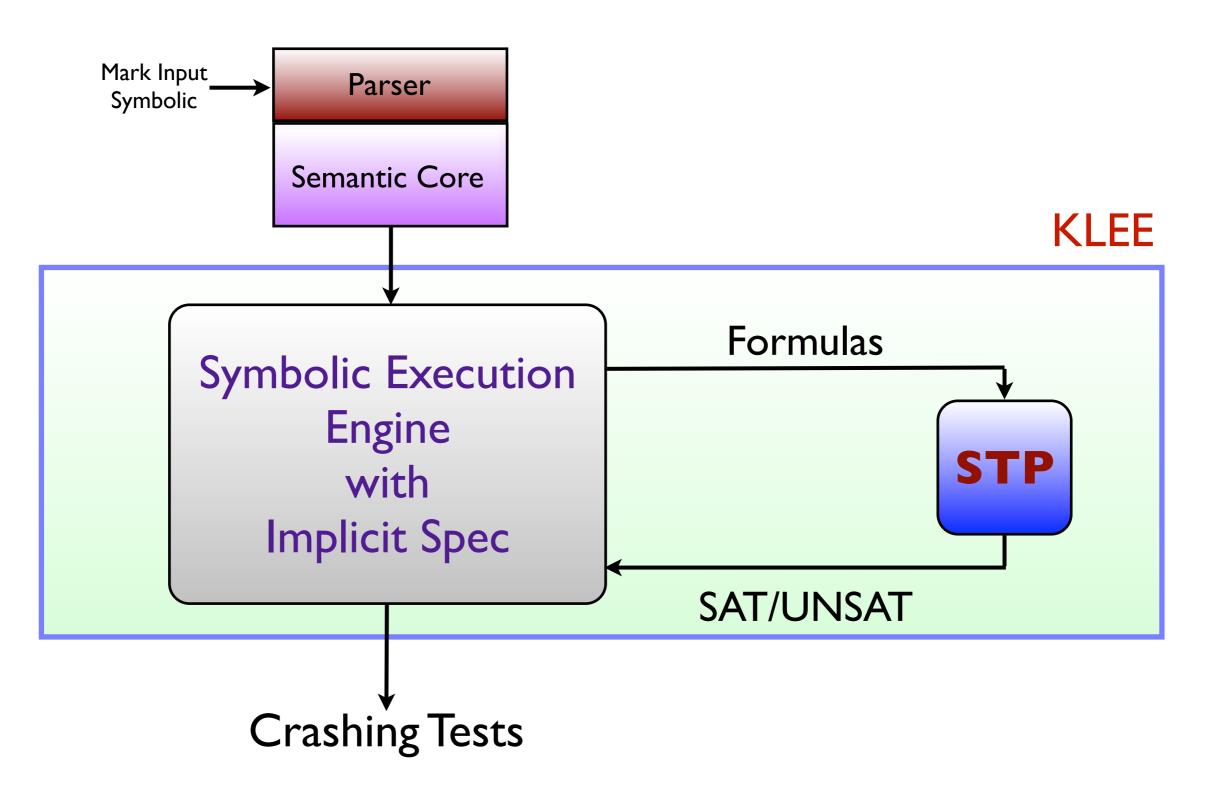
  while (i++ < process(len_field)) {
    //I. Integer overflow causing NULL deref
    //2. Buffer overflow
    *(ptr+i) = process_data(*(data_field+i));
  }
}</pre>
```

Equivalent Logic Formula derived using symbolic execution

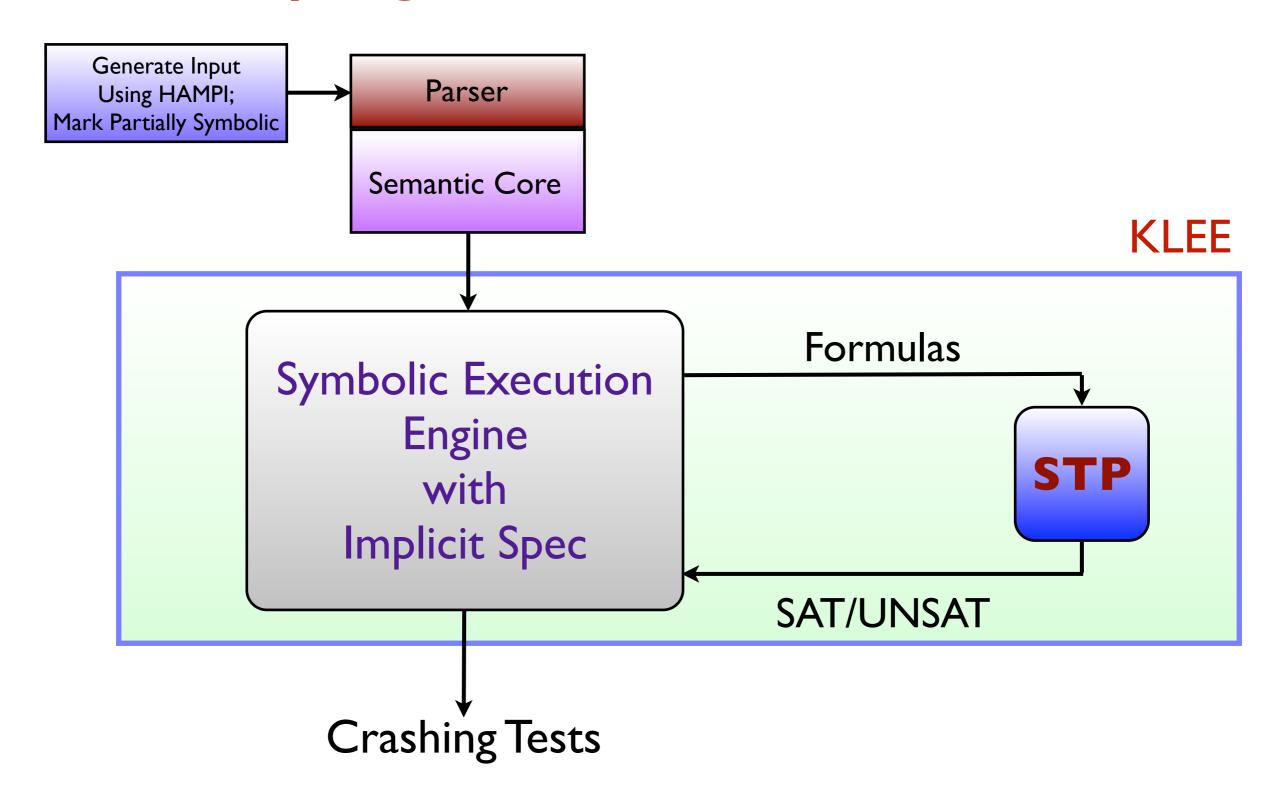
```
data_field, mem_ptr : ARRAY;
len_field : BITVECTOR(32); // symbolic
i, j, ptr : BITVECTOR(32); // symbolic
.
.
mem_ptr[ptr+i] = process_data(data_field[i]);
mem_ptr[ptr+i+I] = process_data(data_field[i+I]);
.
.
//INTEGER OVERFLOW QUERY
0 <= j <= process(len_field);
ptr + i + j = 0?</pre>
```

- Formula captures computation
- Tester attaches formula to capture spec

# HAMPI: Result 4 Helping KLEE Pierce Parsers



# HAMPI: Result 4 Helping KLEE Pierce Parsers



# HAMPI: Result 4 Helping KLEE Pierce Parsers

Klee provides API to place constraints on symbolic inputs

• Manually writing constraints is hard

• Specify grammar using HAMPI, compile to C code

Particularly useful for programs with highly-structured inputs

• 2-5X improvement in line coverage

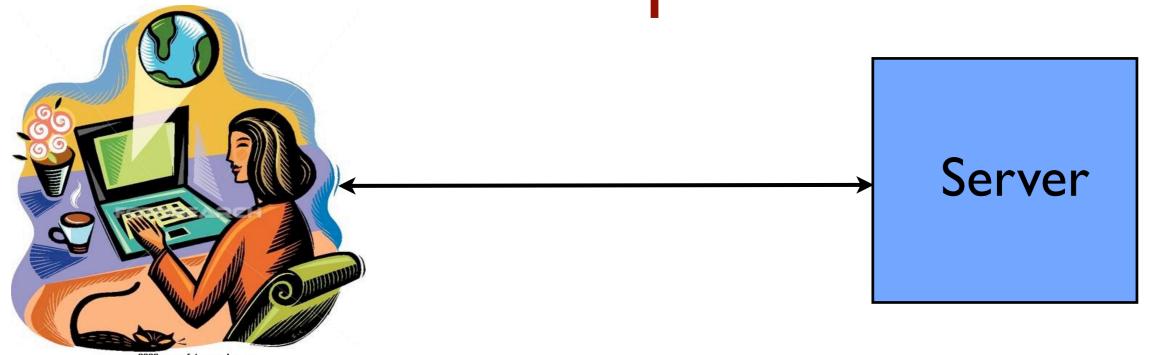
## Impact of Hampi: Notable Projects

Category	Research Project	Project Leader/Institution	
Static Analysis	SQL-injection vulnerabilities	Wasserman & Su/UC, Davis	
Security Testing	Ardilla for PHP (SQL injections, cross-site scripting)	Kiezun & Ernst/MIT	
Concolic Testing	Testing Klee Kudzu NoTamper  Engler & Cadar/Stanford Saxena & Song/Berkeley Bisht & Venkatakrishnan/U Ch		
New Solvers	Kaluza	Saxena & Song/Berkeley	

## Impact of Hampi: Notable Projects

Tool Name	<u>Description</u>	Project Leader/ Institution
Kudzu	JavaScript Bug Finder & Vulnerability Detector	Saxena Akhawe Hanna Mao McCamant Song/Berkeley
NoTamper	Parameter Tamper Detection	Bisht Hinrichs/U of Chicago Skrupsky Bobrowicz Vekatakrishnan/ U. of Illinois, Chicago

## Impact of Hampi: Notable Projects NoTamper



- Client-side checks (C), no server checks
- Find solutions  $S_1, S_2,...$  to C, and solutions  $E_1, E_2,...$  to  $\sim C$  by calling HAMPI
- E<sub>1</sub>,E<sub>2</sub>,... are candidate exploits
- Submit (SI, EI),... to server
- If server response same, ignore
- If server response differ, report error

## Related Work (Practice)

Tool Name	Project Leader/ Institution	Comparison with HAMPI
Rex	Bjorner, Tillman, Vornkov et al. (Microsoft Research, Redmond)	<ul> <li>HAMPI         <ul> <li>+ Length+Replace(s<sub>1</sub>,s<sub>2</sub>,s<sub>3</sub>)</li> <li>- CFG</li> </ul> </li> <li>Translation to int. linear arith. (Z3)</li> </ul>
Mona	Karlund et al. (U. of Aarhus)	<ul> <li>Can encode HAMPI &amp; Rex</li> <li>User work</li> <li>Automata-based</li> <li>Non-elementary</li> </ul>
DPRLE	Hooimeijer (U. of Virginia)	• Regular expression constraints

## Related Work (Theory)

Result	Person (Year)	<u>Notes</u>
Undecidability of Quantified Word Equations	Quine (1946)	Multiplication reduced to concat
Undecidability of Quantified Word Equations with single alternation	Durnev (1996), G. (2011)	2-counter machines reduced to words with single quantifier alter.
Decidability (PSPACE) of QF Theory of Word Equations	Makanin (1977) Plandowski (1996, 2002/06)	Makanin result very difficult Simplified by Plandowski
Decidability (PSPACE- complete) of QF Theory of Word Equations + RE	Schultz (1992)	RE membership predicate
QF word equations + Length() (?)	Matiyasevich (1971)	Unsolved Reduction to Diophantine
QF word equations in solved form + Length() + RE	G. (2011)	Practical

### Future of HAMPI & STP

- HAMPI will be combined with STP
  - Bit-vectors and Arrays
  - Integer/Real Linear Arithmetic
  - Uninterpreted Functions
  - Strings
  - Floating Point
  - Non-linear
- Additional features planned in STP
  - UNSAT Core
  - Quantifiers
  - Incremental
  - DPLL(T)
  - Parallel STP
  - MAXSMT?
- Extensibility and hackability by non-expert

## Future of Strings

### Strings SMTization effort started

- Nikolaj Bjorner, G.
- Andrei Voronkov, Ruzica Piskac, Ting Zhang
- Cesare Tinelli, Clark Barrett, Dawn Song, Prateek Saxena, Pieter Hooimeijer, Tim Hinrichs

### SMT Theory of Strings

- Alphabet (UTF, Unicode,...)
- String Constants and String Vars (parameterized by length)
- Concat, Extract, Replace, Length Functions
- Regular Expressions, CFGs (Extended BNF)
- Equality, Membership Predicate, Contains Predicate

### Applications

- Static/Dynamic Analysis for Vulnerability Detection
- Security Testing using Concolic Idea
- Formal Methods
- Synthesis

## Conclusions & Take Away

- SMT solvers essential for testing, analysis, verification,...
- Core SMT ideas
  - Combinations
  - DPLL(T)
  - Over/Under approximations (CEGAR,...)
  - SAT solvers
- Future of SMT solvers
  - SMT + Languages
  - SMT + Synthesis
  - Parallel SAT/SMT
- Demand for even richer theories
  - Attribute grammars
  - String theories with length

### **Modern SMT Solver References**

#### These websites and handbook have all the references you will need

- I. Armin Bierre, Marijn Heule, Hans van Maaren, and Toby Walsh (Editors). *Handbook of Satisfiability*. 2009. IOS Press. <a href="http://www.st.ewi.tudelft.nl/sat/handbook/">http://www.st.ewi.tudelft.nl/sat/handbook/</a>
- 2. SAT Live: <a href="http://www.satlive.org/">http://www.satlive.org/</a>
- 3. SMT LIB: http://www.smtlib.org/
- 4. SAT/SMT summer school: <a href="http://people.csail.mit.edu/vganesh/summerschool/">http://people.csail.mit.edu/vganesh/summerschool/</a>



#### **Topics covered in Lecture 1**

#### Motivation for SAT/SMT solvers in software engineering

- Software engineering (SE) problems reduced to logic problems
- Automation, engineering, usability of SE tools through solvers

#### **☑** High-level description of the SAT/SMT problem & logics

- Rich logics close to program semantics
- Demonstrably easy to solve in many practical cases

#### Modern SAT solver architecture & techniques

- DPLL search, shortcomings
- Modern CDCL SAT solver: propagate (BCP), decide (VSIDS), conflict analysis, clause learn, backJump,
- Termination, correctness
- Big lesson: learning from mistakes

#### **Topics covered in Lecture 2**

#### Modern SMT solver architecture & techniques

- Rich logics closer to program semantics
- DPLL(T), Combinations of solvers, Over/under approximations

#### My own contributions: STP & HAMPI

- Abstraction-refinement for solving
- Bounded logics

#### **☑** SAT/SMT-based applications

- Dynamic systematic testing
- Static, dynamic analysis for vulnerability detection

#### **☑** Future of SAT/SMT solvers

## Key Contributions http://people.csail.mit.edu/vganesh

<u>Name</u>	Key Concept	<u>Impact</u>	<u>Pubs</u>
STP Bit-vector & Array Solver <sup>1,2</sup>	Abstraction-refinement for Solving	Concolic Testing	CAV 2007 CCS 2006 TISSEC 2008
<b>HAMPI</b> String Solver <sup>I</sup>	App-driven Bounding for Solving	Analysis of Web Apps	ISSTA 2009 <sup>3</sup> TOSEM 2011 (CAV 2011)
Taint-based Fuzzing	Information flow is cheaper than concolic	Scales better than concolic	ICSE 2009
Automatic Input Rectification	Acceptability Envelope: Fix the input, not the program	New way of approaching SE	Under Submission

- 1. 100+ research projects use STP and HAMPI
- 2. STP won the SMTCOMP 2006 and 2010 competitions for bit-vector solvers
- 3. HAMPI: ACM Best Paper Award 2009
- 4. Retargetable Compiler (DATE 1999)
- 5. Proof-producing decision procedures (TACAS 2003)
- 6. Error-finding in ARBAC policies (CCS 2011)